DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING ANNAMACHARYA INSTITUTE OF TECHNOLOGY AND SCIENCE:: RAJAMPETA VALUE ADDED COURSE ON BASICS FOR MATLAB PROGRAMMING

VAC Coordinator: P.Syamala devi, Assistant Professor, Dept. of ECE.

Course Structure

| Course | Class | No. Of Students | Duration | Starting |
|---------------------------|-----------------------|-----------------|----------|-----------------------------|
| | | | | Date |
| Introduction to MATLAB | II B.Tech I sem | 85 | 30 hours | 13.08.2019 TO 16.08.2019 |

Prerequisite:

- 1. C Programming, Basics of Engineering Mathematics and respective equations.
- 2. No prior knowledge of MATLAB is required. Basic computer literacy is expected.

Course Objectives:

- 1. To Impart the Knowledge to the students with MATLAB software
- 2. To provide a working introduction to the MATLAB technical computing environment.
- 3. To introduce students the use of a high-level programming language, MATLAB.

Topics to be covered:

- 1. Basics of MATLAB and MATLAB Compiler
 - ✓ The MATLAB user interface
 - ✓ Working with MATLAB data types
 - ✓ Creating matrices and arrays
 - ✓ Operators and control statements
 - ✓ Using scripts and functions
 - ✓ Data import and export
 - ✓ Using the graphical features
- 2. Programming with simple examples

Learning Resources and References: These are some of the links and books which can help students in increasing their knowledge base and clarification of the doubts. Please visit the links and refer the books to explore the information given:

- [1] http://www.eng-tips.com/threadminder.cfm?pid=575
- [2] http://www.matlabtutorials.com/mathforum/
- [3] http://www.mathworks.in/matlabcentral.
- [4] http://www.cfd-online.com/Forums/tags/matlab.html
- [5] http://diydrones.com/forum/topic/listForTag?tag=Matlab
- [6] MATLAB Manuals and Hand books
- [7] Duane Hanselman, Bruce Little Field "Mastering MATLAB 7", Pearson Education India

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING ANNAMACHARYA INSTITUTE OF TECHNOLOGY AND SCIENCE:: RAJAMPETA VALUE ADDED COURSE ON ASSERTION BASED VLSI DESIGN

VAC Coordinator: M.Hanumanthu, Assistant Professor, Dept. of ECE.

Course Structure:

| Course | Class | No. | of | Duration | Starting Date |
|-------------|--------|----------|----|-------------|---------------|
| | | students | | | |
| Assertion | III. | 120 | | 3 hrs/week | 16.09.2019 |
| Based | B.Tech | | | (Maximum of | ТО |
| VLSI Design | | | | 30 | 17.09.2019 |
| | | | | hours) | |

Prerequisite:

This course has no specific prerequisites. However some familiarities with the following are especially helpful.

- Verilog/VHDL Programming
- HDL Simulation using EDA tools
- Digital Design Concepts

Course Objectives:

The goal of this course is to familiarize students with the concepts and practical skills required to successfully program and Verify digital systems using HDLs. After finishing the course, students should feel comfortable in building their own digital systems and verifying those using assertions.

About ABV:

Assertion-Based Verification is a methodology for improving the effectiveness of a verification environment in which designers use assertions to capture specific design intent and, either through simulation, formal verification, or emulation of these assertions, verify that the design correctly implements that intent.

With language and tool support for assertions widely available, designers and verification engineers have embraced ABV methodologies to improve design quality and verification productivity. Moreover, designers have found assertions to be invaluable in the debugging process. A comprehensive ABV flow leverages assertion libraries and user-defined assertions that are used in simulation, formal verification, and emulation or FPGA-based prototyping.

Topics to be covered:

- Basics of HDL programming
- Programming and Verification using Questasim
- ➢ Introduction to ABV
 - Benefits of ABV
 - ABV Methodology
- System Verilog Assertions
 - Formal Arguments
 - Local Variables
 - Multiple Clocks
 - Data Integrity
 - Glitch Detection
 - Timing Checks
 - Timing Coverage

References:

- 1. http://www.mentor.com/products/fv/methodologies.
- 2. https://verificationacademy.com/courses/assertion-based verification.
- $3. \ http://www.cadence.com/products/fv/Pages/abv_flow$

Course outcome:

After completion of this course, the student can able to implement assertion based verification using assertions.

Assessment:

- 1. Every student has to undergo periodic tests.
- 2. At the end, each student has to give a presentation on a topic covered in this course.

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING ANNAMACHARYA INSTITUTE OF TECHNOLOGY AND SCIENCE:: RAJAMPETA VALUE ADDED COURSE ON ADVANCED VLSI CONCEPTS

Convener: Dr. CH. Nagaraju, HOD, Dept.of ECEResource Person: S. Nazeer HussainTarget Students: III Year ECE studentsDate and Time: 10.02.2020 TO 12.02.2020.

Registration: Interested candidates can register for the Value Added Course contact resource person however attendance mandatory.

Course Structure:

| | c bil uctul c | | |
|--|---------------|----------|----------------|
| Course | Class | No. of | Duratio |
| | | students | n |
| 1.Impact of VLSI on Modern Technology | | | |
| | III | 112 | 6 hrs/week |
| 2. VLSI Back end and front end design flows | ECE | | (Maximum of 12 |
| Importance of VLSI –EDA tools | | | hours) |
| | | | |
| 3. Importance of Optimization Tools in VLSI | | | |
| Design | | | |
| | | | |
| 4. Designing of basic Digital Circuits using | | | |
| GDK components and their programming | | | |
| through VHDL | | | |
| | | | |
| 5. Importance of VLSI in Medical electronics | | | |
| | | | |
| 6.Lab sessions on Tanner tools and Xilinx | | | |
| tools | | | |

Prerequisite:

This course has no specific prerequisites. However some familiarities with the following are especially helpful.

- VHDL Programming
- HDL Simulation using EDA tools
- Digital Design Concepts

Course Objectives:

The goal of this course is to familiarize students with the concepts and practical skills required to successfully program and Verify digital systems using HDLs. After finishing the course, students should feel comfortable in building their own digital systems and verifying those.

Course outcome:

After completion of this course, the student can able to implement different digital design circuits using GDK files and EDA tools.

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING ANNAMACHARYA INSTITUTE OF TECHNOLOGY AND SCIENCE:: RAJAMPETA VALUE ADDED COURSE ON ADVANCES IN DIGITAL SIGNAL PROCESSING

VAC Coordinator: Dr. CH. Nagaraju, Assistant Professor, Dept. of ECE. VAC Instructors: S.Fayaz Begum.

Course Structure:

| Course | Class | No. of | Durati | Starting Date |
|---------------------|-----------|----------|-------------|---------------|
| | | students | on | |
| | | | | 03.02.2020 to |
| Advances in Digital | IV.B.Tech | 98 | 3 hrs/week | 05.02.2020 |
| Signal Processing | | | (Maximum of | |
| | | | 30 hours) | |

Prerequisite:

- Signals and Systems
- Digital Signal Processing

Course Objectives:

The goal of this course is to familiarize students with the concepts required to gain Knowledge on Advances in Digital Signal Processing.

Topics to be covered:

- Discrete-time Signals and LTI Systems
- Convolution and correlation; random signals; linear time-invariant systems transforms.
- Sampling and Finite-precision Effects
- AD/DA; aliasing, quantization, oversampling.
- Filter Design and Filter Structures
- FIR and IIR filter design implementation.
- Multirate DSP
- Interpolation and decimation; filter bank design; poly phase structures.

Learning References:

- 1. Statistical Digital Signal processing and Modeling, (Hayes), JohnWiley.
- 2. Advanced Digital Signal Processing, (Proakis, Rader, Ling &Nikias) Maxwell Macmillan International, 1992.



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING ANNAMACHARYA INSTITUTE OF TECHNOLOGY AND SCIENCE:: RAJAMPET

VALUE ADDED COURSES LIST 2019-20

II B.Tech ECE

| S.No | H.T.No | Student Name | Title | Date |
|------|------------|-------------------------------|-------------------|---------------|
| 1 | 18701A0401 | ABHINAV KUMAR REDDY GADDAM | Basics for MATLAB | 13.08.2019 TO |
| | 18701A0401 | ABHINAV KUMAR REDDY GADDAM | Programming | 16.08.2019 |
| 2 | 18701A0402 | ADI LAKSHMI DEVI GAJJALA | Basics for MATLAB | 13.08.2019 TO |
| | 18701A0402 | | Programming | 16.08.2019 |
| 3 | 18701A0403 | AFRIN SHAIK | Basics for MATLAB | 13.08.2019 TO |
| | 18701A0403 | | Programming | 16.08.2019 |
| 4 | 18701A0404 | AKHILA CHAPPIDI | Basics for MATLAB | 13.08.2019 TO |
| | 18701A0404 | ARHILA CHAPPIDI | Programming | 16.08.2019 |
| 5 | 18701A0405 | AKHILA GOLLA | Basics for MATLAB | 13.08.2019 TO |
| | 18701A0405 | AKHILA GOLLA | Programming | 16.08.2019 |
| 6 | 18701A0406 | AKHILA SEELAM | Basics for MATLAB | 13.08.2019 TO |
| | 18701A0400 | | Programming | 16.08.2019 |
| 7 | 18701A0407 | ANIL KUMAR PALURU CHAKALI | Basics for MATLAB | 13.08.2019 TO |
| | 18701A0407 | ANIE KOWAR PALORO CHARALI | Programming | 16.08.2019 |
| 8 | 18701A0408 | ANIL KUMAR REDDY CHENCHIREDDY | Basics for MATLAB | 13.08.2019 TO |
| | 18701A0408 | ANE KOMAK KEDDT CHENCHIKEDDT | Programming | 16.08.2019 |
| 9 | 18701A0409 | ANIL KUMAR REDDY KETHIREDDY | Basics for MATLAB | 13.08.2019 TO |
| | 18701A0409 | | Programming | 16.08.2019 |
| 10 | 18701A0410 | ANJALI VADLA | Basics for MATLAB | 13.08.2019 TO |
| | 10701A0410 | | Programming | 16.08.2019 |
| 11 | 18701A0411 | ANUSHA G | Basics for MATLAB | 13.08.2019 TO |
| | 10/UIAU411 | | Programming | 16.08.2019 |
| 12 | 18701A0412 | ARCHANA SONTIMALI | Basics for MATLAB | 13.08.2019 TO |
| | 10/01A0412 | | Programming | 16.08.2019 |

| 13 | 4070440440 | | Basics for MATLAB | 13.08.2019 TO |
|----|------------|-----------------------------|-------------------|---------------|
| | 18701A0413 | ARSHIA SHAJARIN SAYYAD | Programming | 16.08.2019 |
| 14 | 4070440444 | | Basics for MATLAB | 13.08.2019 TO |
| | 18701A0414 | ASHOK KUMAR REDDY THOTTI | Programming | 16.08.2019 |
| 15 | 1070140415 | ASIF SHAIK | Basics for MATLAB | 13.08.2019 TO |
| | 18701A0415 | ASIF SHAIK | Programming | 16.08.2019 |
| 16 | 1070140416 | | Basics for MATLAB | 13.08.2019 TO |
| | 18701A0416 | ASLAM BASHA SHAIK | Programming | 16.08.2019 |
| 17 | 18701A0417 | AVINASH GUNTURU | Basics for MATLAB | 13.08.2019 TO |
| | 18701A0417 | AVINASH GUNTURU | Programming | 16.08.2019 |
| 18 | 1070140410 | | Basics for MATLAB | 13.08.2019 TO |
| | 18701A0418 | BANDHAVI DUBBOLLA | Programming | 16.08.2019 |
| 19 | 18701A0419 | BHARGAVA REDDY MOLAKATHALLA | Basics for MATLAB | 13.08.2019 TO |
| | 18701A0419 | BHARGAVA REDDI MOLAKATHALLA | Programming | 16.08.2019 |
| 20 | 18701A0470 | MAHESH AKULA | Basics for MATLAB | 13.08.2019 TO |
| | 18701A0470 | MARESH AKULA | Programming | 16.08.2019 |
| 21 | 18701A0471 | MAHESWAR REDDY AKEPATI | Basics for MATLAB | 13.08.2019 TO |
| | 16/01A04/1 | | Programming | 16.08.2019 |
| 22 | 18701A0472 | MAHESWARI CHILLA | Basics for MATLAB | 13.08.2019 TO |
| | 16701A0472 | | Programming | 16.08.2019 |
| 23 | 18701A0473 | MAHITHA YAGATEELA | Basics for MATLAB | 13.08.2019 TO |
| | 16701A0475 | | Programming | 16.08.2019 |
| 24 | 18701A0474 | MANASA TURAKA | Basics for MATLAB | 13.08.2019 TO |
| | 18701A0474 | MANASA TURAKA | Programming | 16.08.2019 |
| 25 | 18701A0475 | MANISHA AYYAPUSETTY | Basics for MATLAB | 13.08.2019 TO |
| | 18/01A04/5 | | Programming | 16.08.2019 |
| 26 | 18701A0476 | MANOJ KUMAR PATIL | Basics for MATLAB | 13.08.2019 TO |
| | 10/01A04/0 | | Programming | 16.08.2019 |
| 27 | 18701A0477 | MARUTHI PRAVEEN SAGABALA | Basics for MATLAB | 13.08.2019 TO |
| | 10/UIAU4// | | Programming | 16.08.2019 |

| 28 | 4070440470 | | Basics for MATLAB | 13.08.2019 TO |
|----|------------|----------------------------------|-------------------|---------------|
| | 18701A0478 | MOHAMMAD AKRAM SYED | Programming | 16.08.2019 |
| 29 | 4070440470 | | Basics for MATLAB | 13.08.2019 TO |
| | 18701A0479 | MOHAMMAD ANAS SHAIK | Programming | 16.08.2019 |
| 30 | 1070100400 | | Basics for MATLAB | 13.08.2019 TO |
| | 18701A0480 | MOHAMMAD SHAIK | Programming | 16.08.2019 |
| 31 | 1070100404 | | Basics for MATLAB | 13.08.2019 TO |
| | 18701A04C4 | SHYLU SUNKESULA | Programming | 16.08.2019 |
| 32 | 18701A04C5 | SIREESHA NARAPUREDDY | Basics for MATLAB | 13.08.2019 TO |
| | 18701A04C5 | SIREESHA NARAPOREDDY | Programming | 16.08.2019 |
| 33 | 1970140406 | | Basics for MATLAB | 13.08.2019 TO |
| | 18701A04C6 | SIVA PRASAD MADANAPALLI | Programming | 16.08.2019 |
| 34 | 18701A04C7 | SIVA RAMA KRISHNA REDDY KARNATI | Basics for MATLAB | 13.08.2019 TO |
| | 16701A04C7 | | Programming | 16.08.2019 |
| 35 | 18701A04C8 | SIVA SREEKAR Y | Basics for MATLAB | 13.08.2019 TO |
| | 16701A04C6 | SIVA SREEKAR F | Programming | 16.08.2019 |
| 36 | 18701A04C9 | SIVARAM MALLELA | Basics for MATLAB | 13.08.2019 TO |
| | 18701A04C9 | SIVARAWI WALLELA | Programming | 16.08.2019 |
| 37 | 18701A04D0 | SOMA SUNDAR REDDY YARAMAREDDY | Basics for MATLAB | 13.08.2019 TO |
| | 16701A04D0 | SOIVIA SOIVDAR REDDT TARAWAREDDT | Programming | 16.08.2019 |
| 38 | 18701A04D1 | SOWJANYA KADIYALA | Basics for MATLAB | 13.08.2019 TO |
| | 10701A04D1 | SOWJANTA KADITALA | Programming | 16.08.2019 |
| 39 | 18701A04D2 | SRAVANI MALLUGARI | Basics for MATLAB | 13.08.2019 TO |
| | 16701A04D2 | SRAVANI MALLOGARI | Programming | 16.08.2019 |
| 40 | 18701A04D3 | SRAVANI YALLATURU | Basics for MATLAB | 13.08.2019 TO |
| | 10/0140403 | | Programming | 16.08.2019 |
| 41 | 18701A04D4 | SREE LAKSHMI BHAVANASI | Basics for MATLAB | 13.08.2019 TO |
| | 10/01A04D4 | | Programming | 16.08.2019 |
| 42 | 18701A04E0 | SUDHA KONETI | Basics for MATLAB | 13.08.2019 TO |
| | 10/01A0420 | | Programming | 16.08.2019 |

| 43 | 4070440454 | | Basics for MATLAB | 13.08.2019 TO |
|----|------------|-------------------------------|-------------------|---------------|
| | 18701A04E1 | SUDHA RANI RANGA | Programming | 16.08.2019 |
| 44 | 4070440450 | | Basics for MATLAB | 13.08.2019 TO |
| | 18701A04E2 | SUDHARSHAN KUMAR CHANDU | Programming | 16.08.2019 |
| 45 | 1070140452 | | Basics for MATLAB | 13.08.2019 TO |
| | 18701A04E3 | SUDHARSHAN REDDY GALI | Programming | 16.08.2019 |
| 46 | | | Basics for MATLAB | 13.08.2019 TO |
| | 18701A04E4 | SUDHEESHNADEVI MULA | Programming | 16.08.2019 |
| 47 | 18701A04E5 | SUMANTH GUTAM | Basics for MATLAB | 13.08.2019 TO |
| | 18701A04E5 | | Programming | 16.08.2019 |
| 48 | 18701A04E6 | | Basics for MATLAB | 13.08.2019 TO |
| | 18701A04E6 | SUMANTH KUMAR KARIDIBUSHIGARI | Programming | 16.08.2019 |
| 49 | 18701A04E7 | SUMANTH NIMMALA | Basics for MATLAB | 13.08.2019 TO |
| | 18/01A04E/ | | Programming | 16.08.2019 |
| 50 | | SUPRAJA YERRAGORLA | Basics for MATLAB | 13.08.2019 TO |
| | 18701A04E8 | SUPRAJA FERRAGURLA | Programming | 16.08.2019 |
| 51 | 18701A04E9 | SUSHMA PANJAGALLA | Basics for MATLAB | 13.08.2019 TO |
| | 16701A04E9 | | Programming | 16.08.2019 |
| 52 | 1070140450 | | Basics for MATLAB | 13.08.2019 TO |
| | 18701A04F0 | SUSMITHA GOVINDU | Programming | 16.08.2019 |
| 53 | 18701A04F1 | SWATHI REDDY R | Basics for MATLAB | 13.08.2019 TO |
| | 10/01A04F1 | SWATHIREDDIR | Programming | 16.08.2019 |
| 54 | 18701A04F2 | THANUJA SRIPATI | Basics for MATLAB | 13.08.2019 TO |
| | 18701A04F2 | | Programming | 16.08.2019 |
| 55 | 18701A04G0 | VENKATA SAI CHINNARI | Basics for MATLAB | 13.08.2019 TO |
| | 10/01A04G0 | | Programming | 16.08.2019 |
| 56 | 18701A04G1 | VENKATA THARUN REDDY GANDHAM | Basics for MATLAB | 13.08.2019 TO |
| | 10/01A04G1 | | Programming | 16.08.2019 |
| 57 | 18701A04G2 | VENU BHARGAV REDDY NOOKA | Basics for MATLAB | 13.08.2019 TO |
| | 10/01A04GZ | | Programming | 16.08.2019 |

| 58 | 4070440400 | | Basics for MATLAB | 13.08.2019 TO |
|----|------------|-------------------------------|-------------------|---------------|
| | 18701A04G3 | VERONIKAMANI AMBOORU | Programming | 16.08.2019 |
| 59 | 4070440464 | | Basics for MATLAB | 13.08.2019 TO |
| | 18701A04G4 | VINAY KUMAR REDDY MALLELA | Programming | 16.08.2019 |
| 60 | 1970140405 | VINITHA PAPPIREDDY | Basics for MATLAB | 13.08.2019 TO |
| | 18701A04G5 | | Programming | 16.08.2019 |
| 61 | 1970140406 | | Basics for MATLAB | 13.08.2019 TO |
| | 18701A04G6 | VISHALAKSHI KOMMA | Programming | 16.08.2019 |
| 62 | 18701A04G7 | VISHNU PRIYA D | Basics for MATLAB | 13.08.2019 TO |
| | 18/01A04G/ | | Programming | 16.08.2019 |
| 63 | 1070100400 | | Basics for MATLAB | 13.08.2019 TO |
| | 18701A04G8 | VISHNU VARDHAN REDDY MALLU | Programming | 16.08.2019 |
| 64 | 18701A04G9 | VIVEKA GANDAVARAPU | Basics for MATLAB | 13.08.2019 TO |
| | 18701A04G9 | | Programming | 16.08.2019 |
| 65 | 18701A04H0 | VYSHNAVI ANKIREDDY PALLI | Basics for MATLAB | 13.08.2019 TO |
| | 18701A04H0 | VISHNAVI ANKIREDDI PALLI | Programming | 16.08.2019 |
| 66 | 18709A0406 | BHARATH TEJA POTHALAM | Basics for MATLAB | 13.08.2019 TO |
| | 18709A0400 | BHARATH TEJA POTHALAM | Programming | 16.08.2019 |
| 67 | 18709A0407 | BHAVYA SOMPALLI | Basics for MATLAB | 13.08.2019 TO |
| | 18709A0407 | BHAV FA SOMPALLI | Programming | 16.08.2019 |
| 68 | 18709A0408 | DEEPA MOPURI | Basics for MATLAB | 13.08.2019 TO |
| | 18709A0408 | DEEPA MOPORI | Programming | 16.08.2019 |
| 69 | 18709A0409 | DEEPANJANI KODATALA | Basics for MATLAB | 13.08.2019 TO |
| | 18709A0409 | DEEPANJANI KODATALA | Programming | 16.08.2019 |
| 70 | 18709A0410 | FAZULULLA SHAIK | Basics for MATLAB | 13.08.2019 TO |
| | 10705A0410 | | Programming | 16.08.2019 |
| 71 | 18709A0411 | GOURI PRIYA CHOWDAM VARAPAKU | Basics for MATLAB | 13.08.2019 TO |
| | 10705A0411 | | Programming | 16.08.2019 |
| 72 | 18709A0412 | HARSHA CHAITANYA YEDDALAPALLI | Basics for MATLAB | 13.08.2019 TO |
| | 10705A0412 | | Programming | 16.08.2019 |

| 73 | 18709A0413 | HEMANTH KUMAR REDDY DUGGIREDDY | Basics for MATLAB | 13.08.2019 TO | | |
|----|------------|--------------------------------|-----------------------------|---------------|--|--|
| | 10/03A0413 | | Programming | 16.08.2019 | | |
| 74 | 18709A0414 | HEY RAMA RAJU JAGADABHI | Basics for MATLAB | 13.08.2019 TO | | |
| | 16709A0414 | | Programming | 16.08.2019 | | |
| 75 | 18709A0415 | INDIVAR MADDINA | Basics for MATLAB | 13.08.2019 TO | | |
| | 18709A0413 | | Programming | 16.08.2019 | | |
| 76 | 18709A0416 | JALEELBASHA SAGINALA | Basics for MATLAB | 13.08.2019 TO | | |
| | 18709A0410 | JALEELBASHA SAGINALA | Programming | 16.08.2019 | | |
| 77 | 18709A0417 | JAYASREE CHUKKA | Basics for MATLAB | 13.08.2019 TO | | |
| | 18709A0417 | JATASREE CHORKA | Programming | 16.08.2019 | | |
| 78 | 18709A0418 | JEEVAN REDDY AMBAVARAM | Basics for MATLAB | 13.08.2019 TO | | |
| | 10709A0410 | JEEVAN REDDT AIVIDAVARAIVI | Programming | 16.08.2019 | | |
| 79 | 18709A0419 | KAVYA SREE MALIKIREDDY | Basics for MATLAB | 13.08.2019 TO | | |
| | 18709A0419 | | Programming | 16.08.2019 | | |
| 80 | 18709A0420 | LAKSHMI PRIYA MUKKAMALLA | Basics for MATLAB | 13.08.2019 TO | | |
| | 18709A0420 | | Programming | 16.08.2019 | | |
| 81 | 18709A0421 | MADHURI KARANAM | Basics for MATLAB | 13.08.2019 TO | | |
| | 18709A0421 | | Programming | 16.08.2019 | | |
| 82 | 18709A0422 | MAMATHA PEDDATHIMMAYYAGARI | Basics for MATLAB | 13.08.2019 TO | | |
| | 18709A0422 | | Programming | 16.08.2019 | | |
| 83 | 18709A0423 | MOULALI UDAYAGIRI | Basics for MATLAB | 13.08.2019 TO | | |
| | 18709A0423 | MOOLALI ODATAGINI | Programming | 16.08.2019 | | |
| 84 | 18709A0424 | MUNIBHARGAV THOTA | Basics for MATLAB | 13.08.2019 TO | | |
| | 18709A0424 | | Programming | 16.08.2019 | | |
| 85 | 18709A0425 | NAVEEN KUMAR REDDY PALURU | Basics for MATLAB | 13.08.2019 TO | | |
| | 18709A0423 | NAVEEN KOWAR REDDT PALORO | Programming | 16.08.2019 | | |
| | III B.Tech | | | | | |
| 1 | 4770440400 | | Assertion Based VLSI Design | 16.09.2019 TO | | |
| | 17701A0403 | AMARANATH CHANAGANI | | 17.09.2019 | | |
| 2 | 17701A0404 | AMRUTHA SREE KORLAKUNTA | Assertion Based VLSI Design | 16.09.2019 TO | | |

| | | | | 17.09.2019 |
|----|------------|------------------------------|-----------------------------|---------------|
| 3 | 17701A0405 | ANDRO MARTIN VIVEK S R | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0405 | ANDRO MARTIN VIVER 3 R | | 17.09.2019 |
| 4 | 17701A0406 | ASMA SYED | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0400 | ASIVIA STED | | 17.09.2019 |
| 5 | 17701A0407 | BABA SAI KISHORE GADARAPU | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0407 | BABA SAI KISHOKE GADAKAFO | | 17.09.2019 |
| 6 | 17701A0408 | BASHA SHAIK | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0408 | BASHA SHAIK | | 17.09.2019 |
| 7 | 17701A0409 | BHARATH KATIKELA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0409 | BHARATH RATIREEA | | 17.09.2019 |
| 8 | 17701A0410 | BHARATH KUMAR NAKKA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0410 | BHARATH KOWAR NARRA | | 17.09.2019 |
| 9 | 17701A0411 | BHARATHKUMARREDDY RAJUPALLI | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0411 | BHARATTIKOWARREDDT RAJOFALLI | | 17.09.2019 |
| 10 | 17701A0412 | BHARGAV REDDY DUMPA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0412 | | | 17.09.2019 |
| 11 | 17701A0413 | BHAVYA SREE OGGU | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0415 | BHAVIA SILL OGGO | | 17.09.2019 |
| 12 | 17701A0414 | CHAKRADHAR RAJU PANDETI | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0414 | | | 17.09.2019 |
| 13 | 17701A0415 | CHANDRAKALA ATMAKUR | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0415 | | | 17.09.2019 |
| 14 | 17701A0416 | CHANDRAKALAVATHI KATREDDY | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0410 | CHANDRAKALAVATTI KATREDDT | | 17.09.2019 |
| 15 | 17701A0417 | CHANDRASEKHAR NAGASWARAM | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1//UIA041/ | | | 17.09.2019 |
| 16 | 17701A0418 | CHARAN KUMAR PAGADALA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0418 | | | 17.09.2019 |
| 17 | 17701A0419 | CHARANKUMAR REDDY LINGALA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0419 | | | 17.09.2019 |

| 18 | 17701A0420 | CHARANTEJA POOLA | Assertion Based VLSI Design | 16.09.2019 TO |
|----|------------|---------------------------|-----------------------------|---------------|
| | 17701A0420 | CHARANTEJA POOLA | | 17.09.2019 |
| 19 | 17701A0421 | DEEPA POLAMREDDY | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0421 | DEEFA FOLAWIREDDT | | 17.09.2019 |
| 20 | 17701A0422 | DHANUSHA THOTA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0422 | | | 17.09.2019 |
| 21 | 17701A0423 | DHARANI ANANDI | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770170423 | | | 17.09.2019 |
| 22 | 17701A0424 | DHARANI BRAMHADEVUNI | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770170424 | | | 17.09.2019 |
| 23 | 17701A0425 | DIVYA ANDRA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0423 | | | 17.09.2019 |
| 24 | 17701A0426 | DIVYA KONETI | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770170420 | | | 17.09.2019 |
| 25 | 17701A0427 | DIVYANJALI GALISETTY | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770170427 | | | 17.09.2019 |
| 26 | 17701A0428 | DOSHNA KOUSALYA ARIGELA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770170420 | | | 17.09.2019 |
| 27 | 17701A0429 | DURGA VARA PRASAD BANDI | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770170425 | | | 17.09.2019 |
| 28 | 17701A0430 | GANGADHAR MANIKALA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770170430 | | | 17.09.2019 |
| 29 | 17701A0431 | GAYATHRI KAKARLA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0451 | | | 17.09.2019 |
| 30 | 17701A0432 | GIREESH KUMAR DEVARAPALLI | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1//01/0492 | | | 17.09.2019 |
| 31 | 17701A0433 | GOVARDHAN MALISETTY | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1//01/0403 | | | 17.09.2019 |
| 32 | 17701A0434 | GURU KISHORE CHITTIBOINA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1//0140404 | | | 17.09.2019 |

| 33 | | | Assertion Based VLSI Design | 16.09.2019 TO |
|----|------------|----------------------------|-----------------------------|---------------|
| | 17701A0435 | GURU PAVANKALYAN UPPALURU | | 17.09.2019 |
| 34 | 4770440406 | | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0436 | GURU SHEKHAR PUPPAM | | 17.09.2019 |
| 35 | 4770440407 | | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0437 | GURUNAVEEN BAPANAPALLE | | 17.09.2019 |
| 36 | 17701A0438 | HARIKA ANUGONDA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0438 | HARIKA ANUGUNDA | | 17.09.2019 |
| 37 | 17701A0439 | HARSHITHA REDDY METTUPALLE | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0439 | HARSHITHA REDDT METTOPALLE | | 17.09.2019 |
| 38 | 17701A0440 | HASEENA BEGAM SHAIK | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0440 | HASEEINA BEGAINI SHAIK | | 17.09.2019 |
| 39 | 17701A0441 | HIMABINDU M | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0441 | | | 17.09.2019 |
| 40 | 17701A0442 | HIMABINDU UMMADIREDDY | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0442 | | | 17.09.2019 |
| 41 | 17701A0443 | ΗΙΜΑΙΑ ΚΟΚΚΑΝΤΙ | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770170445 | | | 17.09.2019 |
| 42 | 17701A0444 | JAGRUTHI PEYYALA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770170444 | JAGROTHITETTALA | | 17.09.2019 |
| 43 | 17701A0445 | JAYA BHARATH REDDY MANDADI | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770170445 | | | 17.09.2019 |
| 44 | 17701A0446 | JYOTHI GANJIKUNTA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0440 | | | 17.09.2019 |
| 45 | 17701A0447 | KARISHMA SHAIK | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1//01/044/ | | | 17.09.2019 |
| 46 | 17701A0448 | KAVERI MALLIREDDY | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770170440 | | | 17.09.2019 |
| 47 | 17701A0449 | KEERTHANA RAMANNA GARI | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770170449 | | | 17.09.2019 |

| 48 | 1770140450 | | Assertion Based VLSI Design | 16.09.2019 TO |
|----|------------|------------------------------|-----------------------------|---------------|
| | 17701A0450 | KHADEEJ UL UMERAH BEPAR | | 17.09.2019 |
| 49 | 17701A0451 | KRANTHI CHEERALA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1//01A0451 | | | 17.09.2019 |
| 50 | 17701A0452 | LAHARI SATTU | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770170452 | | | 17.09.2019 |
| 51 | 17701A0453 | LAKSHMI JUTURU | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770170455 | | | 17.09.2019 |
| 52 | 17701A0454 | LAKSHMI KANTHAMMA MANGALI | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770170454 | | | 17.09.2019 |
| 53 | 17701A0455 | LAKSHMI PRASANNA KARIMEDDALA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770170455 | | | 17.09.2019 |
| 54 | 17701A0456 | LAKSHMI PRASANNA NARLA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770170450 | | | 17.09.2019 |
| 55 | 17701A0464 | MALLIKARJUNA BAPANAPALLI | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770170404 | | | 17.09.2019 |
| 56 | 17701A0465 | MAMATHA BUSIREDDY | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770170405 | | | 17.09.2019 |
| 57 | 17701A0466 | MANIKANTA MEDIKONDU | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0400 | | | 17.09.2019 |
| 58 | 17701A0467 | MEGHANA GANDI | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1//01A040/ | | | 17.09.2019 |
| 59 | 17701A0468 | MEGHANA GUTHA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0408 | MEGHANA GUTHA | | 17.09.2019 |
| 60 | 17701A0469 | NAADIYA SHAIK | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1//UIA0409 | | | 17.09.2019 |
| 61 | 1770140470 | NAGA JYOTHI UMMADIREDDY | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0470 | | | 17.09.2019 |
| 62 | 1770140471 | | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0471 | NARASIMHULU CHINTHAMANI | | 17.09.2019 |

| 63 | | | Assertion Based VLSI Design | 16.09.2019 TO |
|----|------------|------------------------------|-----------------------------|---------------|
| | 17701A0472 | NAVEEN AREPALLI | | 17.09.2019 |
| 64 | 4770440470 | | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0473 | NAVEEN KALLURU | | 17.09.2019 |
| 65 | 1770100474 | NAVEEN KUMAR REDDY GANDLURU | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0474 | NAVEEN KUMAR REDDY GANDLORU | | 17.09.2019 |
| 66 | 17701A0475 | NAVEEN KUMAR REDDY GOPIREDDY | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1//01A04/5 | NAVEEN KOMAR REDDY GOPIREDDY | | 17.09.2019 |
| 67 | 17701A0476 | NAVEEN KUMAR SIDDAM | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1//01A04/0 | NAVEEN KOIMAR SIDDAMI | | 17.09.2019 |
| 68 | 17701A0477 | NAVYA DANASI | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1//01A04// | NAVIA DANASI | | 17.09.2019 |
| 69 | 17701A0478 | NAVYA LAKSHMI DONTHU | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1//01A04/8 | | | 17.09.2019 |
| 70 | 17701A0479 | | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0479 | NAYAB RASOOL SHAIK | | 17.09.2019 |
| 71 | 17701A0480 | NEELIMA NAGARURI | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0460 | | | 17.09.2019 |
| 72 | 1770140401 | | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0481 | NIHARIKA KANDUKURI | | 17.09.2019 |
| 73 | 17701A0482 | NIKHIL DASARI | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0462 | | | 17.09.2019 |
| 74 | 1770140402 | | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0483 | NIKHIL KENCHUGUNDU | | 17.09.2019 |
| 75 | 1770100494 | NIRVISHA GANGIREDDY | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0484 | | | 17.09.2019 |
| 76 | | | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0485 | NITESH KUMAR AMINENI | | 17.09.2019 |
| 77 | 1770140400 | | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0486 | OBULREDDY GODI | | 17.09.2019 |

| 78 | | | Assertion Based VLSI Design | 16.09.2019 TO |
|----|------------|--------------------------------|-----------------------------|---------------|
| | 17701A0487 | PAAVANA KUMAR CHINTALAPALLI | 0 | 17.09.2019 |
| 79 | | | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0488 | PAVAN KALYAN MAJJIGA | | 17.09.2019 |
| 80 | 1770140400 | | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0489 | PAVANTEJA REDDY GADI | | 17.09.2019 |
| 81 | 1770140400 | | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0490 | PRADEEP REDDY SAREDDY | | 17.09.2019 |
| 82 | 17701A0491 | PRASANNA NAGENDRA BANNEL | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0491 | PRASANNA NAGENDRA BANNEL | | 17.09.2019 |
| 83 | 17701A0492 | PRAVEEN KUMAR KAMALAPURAM | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0492 | | | 17.09.2019 |
| 84 | 17701A0493 | PRAVEEN KUMAR REDDY DUGGIREDDY | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0495 | | | 17.09.2019 |
| 85 | 17701A0494 | PRAVEENA KODURU | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A0494 | | | 17.09.2019 |
| 86 | 17701A04D7 | SUBRAMANYAM RAJU SANGARAJU | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A04D7 | | | 17.09.2019 |
| 87 | 17701A04D8 | SUCHARITHA MEEGADA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A04D0 | | | 17.09.2019 |
| 88 | 17701A04D9 | SUDHAKAR SANA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A04D3 | | | 17.09.2019 |
| 89 | 17701A04E0 | SUMANTH BHEEMA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A04L0 | | | 17.09.2019 |
| 90 | 17701A04E1 | SUNEETHA CHINNARAMANNAGARI | Assertion Based VLSI Design | 16.09.2019 TO |
| | I//UIAU4EI | | | 17.09.2019 |
| 91 | 17701A04E2 | SUPRAJA KOLLU | Assertion Based VLSI Design | 16.09.2019 TO |
| | I//UIAU4LZ | | | 17.09.2019 |
| 92 | 17701A04E3 | SURENDRABABU VANNEM | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A04E3 | | | 17.09.2019 |

| 93 | 4770440454 | | Assertion Based VLSI Design | 16.09.2019 TO |
|-----|-------------|------------------------------|-----------------------------|---------------|
| | 17701A04E4 | SURESH KUMAR REDDY DESIREDDY | | 17.09.2019 |
| 94 | 17701A04E5 | SWAROOP KUMAR SAILESWARAM | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17701A04E5 | SWAROOP KUIVIAR SAILESWARAW | | 17.09.2019 |
| 95 | 17701A04E6 | SWATHI DERANGULA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770170420 | | | 17.09.2019 |
| 96 | 17701A04E7 | TEJASWINI YANAMALA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770170427 | | | 17.09.2019 |
| 97 | 17709A0413 | MANISHA SOMALA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17703710113 | | | 17.09.2019 |
| 98 | 17709A0414 | MANJULA BELLALA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17703710111 | | | 17.09.2019 |
| 99 | 17709A0415 | MOHITHA SAI DUDDUKUNTA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17703700113 | | | 17.09.2019 |
| 100 | 17709A0416 | MUNI LALITH KUMAR VOMMI | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17703710110 | | | 17.09.2019 |
| 101 | 17709A0417 | MUNI PREM KUMAR VOMMI | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17703710117 | | | 17.09.2019 |
| 102 | 17709A0418 | NAVEEN VENKATASAI BANALA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17703710110 | | | 17.09.2019 |
| 103 | 17709A0419 | PRASANNA LINGAM | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17703700113 | | | 17.09.2019 |
| 104 | 17709A0421 | PRATHYUSHA SOMALA | Assertion Based VLSI Design | 16.09.2019 TO |
| | 17705710421 | | | 17.09.2019 |
| 105 | 17709A0422 | PRAVALLIKA MUMMADI | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770570722 | | | 17.09.2019 |
| 106 | 17709A0423 | PREETHI POCHAMIREDDY | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770570423 | | | 17.09.2019 |
| 107 | 17709A0424 | PREMKUMAR REDDY MUMMADI | Assertion Based VLSI Design | 16.09.2019 TO |
| | 1770570424 | | | 17.09.2019 |

| 108 | 17709A0425 | RANI BOLLA | Assertion Based VLSI Design | 16.09.2019 TO | | |
|-----|------------|------------------------------|-----------------------------|---------------|--|--|
| | 1770570425 | | | 17.09.2019 | | |
| 109 | 17709A0426 | REDDY BHANU RAMI REDDY GARI | Assertion Based VLSI Design | 16.09.2019 TO | | |
| | 17709A0420 | REDDI BHANO RAINI REDDI GARI | | 17.09.2019 | | |
| 110 | 4770040407 | | Assertion Based VLSI Design | 16.09.2019 TO | | |
| | 17709A0427 | SAI KUMAR MODAGALA | | 17.09.2019 | | |
| 111 | | | Assertion Based VLSI Design | 16.09.2019 TO | | |
| | 17709A0428 | SAILESHNANDAN REDDY VADDI | | 17.09.2019 | | |
| 112 | | | Assertion Based VLSI Design | 16.09.2019 TO | | |
| | 17709A0429 | SAINATH MANDLI | | 17.09.2019 | | |
| 113 | | | Assertion Based VLSI Design | 16.09.2019 TO | | |
| _ | 17709A0430 | SHAHEENA SHAIK | Ű | 17.09.2019 | | |
| 114 | | | Assertion Based VLSI Design | 16.09.2019 TO | | |
| | 17709A0432 | SREEVATSAV REDDY GAJJALA | | 17.09.2019 | | |
| 115 | | | Assertion Based VLSI Design | 16.09.2019 TO | | |
| | 17709A0433 | SRI HARSHINI GURRAMKONDA | | 17.09.2019 | | |
| 116 | | | Assertion Based VLSI Design | 16.09.2019 TO | | |
| 110 | 17709A0434 | TEJASREE RACHURI | | 17.09.2019 | | |
| 117 | | | Assertion Based VLSI Design | 16.09.2019 TO | | |
| | 17709A0435 | TEJESH SANISETTI | | 17.09.2019 | | |
| 118 | | | Assertion Based VLSI Design | 16.09.2019 TO | | |
| 110 | 17709A0436 | THUSHARA BHARGAVI SINDHURI | | 17.09.2019 | | |
| 119 | | | Assertion Based VLSI Design | 16.09.2019 TO | | |
| 117 | 17709A0437 | TRINATH C | | 17.09.2019 | | |
| 120 | | | Assertion Based VLSI Design | 16.09.2019 TO | | |
| 120 | 17709A0438 | VENKATA RANGA SARANYA EPURI | | 17.09.2019 | | |
| | | | | | | |
| | III B.Tech | | | | | |
| 1 | | | Advanced VLSI Concepts | 10.02.2020 TO | | |
| 1 | 17701A0401 | ABDUL KALAM SHAIK | | 12.02.2020 | | |
| | | | | 12.02.2020 | | |

| 2 | 1770140402 | | Advanced VLSI Concepts | 10.02.2020 TO |
|----|-------------|-----------------------------|------------------------|---------------|
| | 17701A0403 | AMARANATH CHANAGANI | | 12.02.2020 |
| 3 | 17701A0404 | AMRUTHA SREE KORLAKUNTA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0404 | AWROTHA SKEE KORLAKONTA | | 12.02.2020 |
| 4 | 17701A0405 | ANDRO MARTIN VIVEK S R | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0405 | | | 12.02.2020 |
| 5 | 17701A0406 | ASMA SYED | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0400 | | | 12.02.2020 |
| 6 | 17701A0407 | BABA SAI KISHORE GADARAPU | Advanced VLSI Concepts | 10.02.2020 TO |
| | 1//01/040/ | | | 12.02.2020 |
| 7 | 17701A0408 | BASHA SHAIK | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0400 | | | 12.02.2020 |
| 8 | 17701A0409 | BHARATH KATIKELA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 1770170405 | | | 12.02.2020 |
| 9 | 17701A0410 | BHARATH KUMAR NAKKA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0410 | | | 12.02.2020 |
| 10 | 17701A0411 | BHARATHKUMARREDDY RAJUPALLI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701/(0411 | | | 12.02.2020 |
| 11 | 17701A0412 | BHARGAV REDDY DUMPA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701/(0412 | | | 12.02.2020 |
| 12 | 17701A0413 | BHAVYA SREE OGGU | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701/(0415 | | | 12.02.2020 |
| 13 | 17701A0414 | CHAKRADHAR RAJU PANDETI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701/0414 | | | 12.02.2020 |
| 14 | 17701A0415 | CHANDRAKALA ATMAKUR | Advanced VLSI Concepts | 10.02.2020 TO |
| | 1//01/0413 | | | 12.02.2020 |
| 15 | 17701A0416 | CHANDRAKALAVATHI KATREDDY | Advanced VLSI Concepts | 10.02.2020 TO |
| | 1770170410 | | | 12.02.2020 |
| 16 | 17701A0417 | CHANDRASEKHAR NAGASWARAM | Advanced VLSI Concepts | 10.02.2020 TO |
| | 1//018041/ | | | 12.02.2020 |

| 17 | 17701A0418 | CHARAN KUMAR PAGADALA | Advanced VLSI Concepts | 10.02.2020 TO |
|----|-------------|------------------------------|------------------------|---------------|
| | 17701A0418 | CHARAN KUMAR PAGADALA | | 12.02.2020 |
| 18 | 17701A0419 | CHARANKUMAR REDDY LINGALA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0419 | CHARANKOWAR REDDT EINGALA | | 12.02.2020 |
| 19 | 17701A0420 | CHARANTEJA POOLA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0420 | | | 12.02.2020 |
| 20 | 17701A0421 | DEEPA POLAMREDDY | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0421 | | | 12.02.2020 |
| 21 | 17701A0422 | DHANUSHA THOTA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701/(0422 | | | 12.02.2020 |
| 22 | 17701A0423 | DHARANI ANANDI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0425 | | | 12.02.2020 |
| 23 | 17701A0424 | DHARANI BRAMHADEVUNI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701/(0424 | | | 12.02.2020 |
| 24 | 17701A0425 | DIVYA ANDRA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 1770170425 | | | 12.02.2020 |
| 25 | 17701A0426 | DIVYA KONETI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 1770170420 | | | 12.02.2020 |
| 26 | 17701A0427 | DIVYANJALI GALISETTY | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701/(0427 | | | 12.02.2020 |
| 27 | 17701A0428 | DOSHNA KOUSALYA ARIGELA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 1770170420 | | | 12.02.2020 |
| 28 | 17701A0474 | NAVEEN KUMAR REDDY GANDLURU | Advanced VLSI Concepts | 10.02.2020 TO |
| | 1//01/04/4 | | | 12.02.2020 |
| 29 | 17701A0475 | NAVEEN KUMAR REDDY GOPIREDDY | Advanced VLSI Concepts | 10.02.2020 TO |
| | 1110140413 | | | 12.02.2020 |
| 30 | 17701A0476 | NAVEEN KUMAR SIDDAM | Advanced VLSI Concepts | 10.02.2020 TO |
| | 1//01/04/0 | | | 12.02.2020 |
| 31 | 17701A0477 | NAVYA DANASI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 1//01/04// | | | 12.02.2020 |

| 32 | 1770140470 | | Advanced VLSI Concepts | 10.02.2020 TO |
|----|------------|-----------------------------|------------------------|---------------|
| | 17701A0478 | NAVYA LAKSHMI DONTHU | | 12.02.2020 |
| 33 | 17701A0479 | NAYAB RASOOL SHAIK | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0479 | NATAB RASOUL SHAIK | | 12.02.2020 |
| 34 | 17701A0480 | NEELIMA NAGARURI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0480 | NEELIMA NAGARORI | | 12.02.2020 |
| 35 | 17701A0481 | NIHARIKA KANDUKURI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0481 | | | 12.02.2020 |
| 36 | 17701A0482 | NIKHIL DASARI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0482 | NIKHIL DASAKI | | 12.02.2020 |
| 37 | 17701A0483 | NIKHIL KENCHUGUNDU | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0485 | NIKHIE KENCHOGONDO | | 12.02.2020 |
| 38 | 17701A0484 | NIRVISHA GANGIREDDY | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0404 | | | 12.02.2020 |
| 39 | 17701A0485 | NITESH KUMAR AMINENI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0485 | | | 12.02.2020 |
| 40 | 17701A0486 | OBULREDDY GODI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0400 | | | 12.02.2020 |
| 41 | 17701A0487 | PAAVANA KUMAR CHINTALAPALLI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 1//01A048/ | | | 12.02.2020 |
| 42 | 17701A0488 | PAVAN KALYAN MAJJIGA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0488 | | | 12.02.2020 |
| 43 | 17701A0489 | PAVANTEJA REDDY GADI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0409 | | | 12.02.2020 |
| 44 | 17701A0490 | PRADEEP REDDY SAREDDY | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0450 | | | 12.02.2020 |
| 45 | 17701A0491 | PRASANNA NAGENDRA BANNEL | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0431 | | | 12.02.2020 |
| 46 | 17701A0492 | PRAVEEN KUMAR KAMALAPURAM | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0492 | | | 12.02.2020 |

| 48 1770: | LAO494 PRAVE | EN KUMAR REDDY DUGGIREDDY ENA KODURU | Advanced VLSI Concepts | 12.02.2020 10.02.2020 TO |
|----------|----------------|---|------------------------|-----------------------------|
| 49 | | ENA KODURU | Advanced VLSI Concepts | |
| 49 | | | | |
| 49 | LA0495 RAAGA | | | 12.02.2020 |
| | LAU495 RAAGA | CHANDANA KASIREDDY | Advanced VLSI Concepts | 10.02.2020 TO |
| 1770. | | CHANDANA KASIKEDDI | | 12.02.2020 |
| 50 1770 | LA0496 RAGHA | VENDRA MAJJARI | Advanced VLSI Concepts | 10.02.2020 TO |
| 1//0. | LAU490 KAGHA | | | 12.02.2020 |
| 51 1770 | | 011.4 | Advanced VLSI Concepts | 10.02.2020 TO |
| 1//0. | LA0497 RAJA G | OLLA | | 12.02.2020 |
| 52 | | | Advanced VLSI Concepts | 10.02.2020 TO |
| 1//01 | LA0498 RAJENI | DRA BANDI | | 12.02.2020 |
| 53 4770 | | | Advanced VLSI Concepts | 10.02.2020 TO |
| 1//01 | LA0499 RAJESV | VARI MULI | | 12.02.2020 |
| 54 | | | Advanced VLSI Concepts | 10.02.2020 TO |
| 1//01 | LA04A0 RAJU A | SPARI | | 12.02.2020 |
| 55 | | | Advanced VLSI Concepts | 10.02.2020 TO |
| 1//02 | A04A1 RAKESI | H NAIDU PAINENI | | 12.02.2020 |
| 56 | | | Advanced VLSI Concepts | 10.02.2020 TO |
| 17702 | LA04A2 RAMYA | A SREE SAMA | | 12.02.2020 |
| 57 | | | Advanced VLSI Concepts | 10.02.2020 TO |
| 17702 | LA04A3 RASI N | ANDALURU | | 12.02.2020 |
| 58 | | | Advanced VLSI Concepts | 10.02.2020 TO |
| 17702 | LA04A4 ROHITI | 1A V | | 12.02.2020 |
| 59 | | | Advanced VLSI Concepts | 10.02.2020 TO |
| 17702 | LA04A5 RUDRA | VEENA THIMMA REDDY | | 12.02.2020 |
| 60 | | | Advanced VLSI Concepts | 10.02.2020 TO |
| 17702 | LA04A6 SAI KRI | SHNA DANDALA | | 12.02.2020 |
| 61 | | | Advanced VLSI Concepts | 10.02.2020 TO |
| 17702 | LA04A7 SAI KU | MAR REDDY GAJJALA | | 12.02.2020 |

| 62 | 17701A04A8 | SAI VARDHAN BOLLA | Advanced VLSI Concepts | 10.02.2020 TO |
|----|-----------------|---------------------------------|------------------------|---------------|
| | 1770170470 | | | 12.02.2020 |
| 63 | 17701A04A9 | SAIKUMAR VAKKANTI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04A9 | SAIKUWAN VAKNANTI | | 12.02.2020 |
| 64 | 17701A04B0 | SAINADH GOPINENI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04B0 | SAINADIT GOFINEINI | | 12.02.2020 |
| 65 | 17701A04B1 | SAIUSHA RAMYA BADRI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04D1 | | | 12.02.2020 |
| 66 | 16701A04E2 | VENKATA SAI KRISHNA KUMAR SUNKU | Advanced VLSI Concepts | 10.02.2020 TO |
| | 10701A04E2 | | | 12.02.2020 |
| 67 | 18705A0406 | KAVYA KUMMARA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 18/05A0400 | KAV TA KUIVIIVIAKA | | 12.02.2020 |
| 68 | 1070540407 | | Advanced VLSI Concepts | 10.02.2020 TO |
| | 18705A0407 | KAVYA SREE KURLA | | 12.02.2020 |
| 69 | 1070540400 | | Advanced VLSI Concepts | 10.02.2020 TO |
| | 18705A0408 | KIRAN KUMAR SAPOLLA | | 12.02.2020 |
| 70 | 1070540400 | | Advanced VLSI Concepts | 10.02.2020 TO |
| | 18705A0409 | MOHAMMAD IMRAN PATNOOL | | 12.02.2020 |
| 71 | 1070540410 | | Advanced VLSI Concepts | 10.02.2020 TO |
| | 18705A0410 | NARENDRA GANDU | | 12.02.2020 |
| 72 | 1070540414 | | Advanced VLSI Concepts | 10.02.2020 TO |
| | 18705A0411 | PRADEEP KUMAR AREMARATI | | 12.02.2020 |
| 73 | 1070540440 | | Advanced VLSI Concepts | 10.02.2020 TO |
| | 18705A0412 | PRANAVI GUMMANA | | 12.02.2020 |
| 74 | 47704 . 0 . 5 0 | | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04B2 | SANDEEP GONGATI | | 12.02.2020 |
| 75 | | | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04B3 | SANDEEP KALABAI | | 12.02.2020 |
| 76 | | | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04B4 | SANDHYARANI GUTTURU | | 12.02.2020 |

| 77 | 17701A04B5 | SASIKUMAR EMMIDI | Advanced VLSI Concepts | 10.02.2020 TO |
|----|------------|-----------------------------|------------------------|---------------|
| | 17701A04B5 | SASIKUMAR EMIMIDI | | 12.02.2020 |
| 78 | 17701A04B6 | SATHISH KUMAR REDDY KALLURI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04B0 | SATHISH KOWAR REDDT KALLORI | | 12.02.2020 |
| 79 | 17701A04B7 | SATYA NARAYANA KAPPALA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04D7 | | | 12.02.2020 |
| 80 | 17701A04B8 | SHANTHA BABU DONKAPPA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A0400 | | | 12.02.2020 |
| 81 | 17701A04B9 | SIREESHA GALI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04D5 | | | 12.02.2020 |
| 82 | 17701A04C0 | SIREESHA KASIREDDY | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04C0 | SIRESHARASIREDDI | | 12.02.2020 |
| 83 | 17701A04C1 | SIREESHA LANGANURU | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04C1 | SINCESTIA LANGANORO | | 12.02.2020 |
| 84 | 17701A04C2 | SIVA JYOSHNA BANDI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04C2 | | | 12.02.2020 |
| 85 | 17701A04C3 | SIVA KUMAR KOPPLA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04C3 | | | 12.02.2020 |
| 86 | 17701A04C4 | SIVA PRASAD PURUM | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04C4 | SIVA PRASAD POROIVI | | 12.02.2020 |
| 87 | 17701A04C5 | SIVA PRIYA UYYALA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04C3 | SIVA PRITA OTTALA | | 12.02.2020 |
| 88 | 17701A04C6 | SIVA SANKAR MANCHALA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04C0 | SIVA SANKAR MANCHALA | | 12.02.2020 |
| 89 | 17701A04C7 | SOMA SUNDHAR VELPULA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 1//UIAU4C/ | JUIVIA JUIVDIAK VELPULA | | 12.02.2020 |
| 90 | 17701A04C8 | SONI KATARI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04C8 | | | 12.02.2020 |
| 91 | 1770140400 | | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04C9 | SPOORTHI JINKA | | 12.02.2020 |

| 92 | 1770110100 | | Advanced VLSI Concepts | 10.02.2020 TO |
|-----|------------|---------------------------------|------------------------|---------------|
| | 17701A04D0 | SRAVAN KUMAR CHINTHAMANI | | 12.02.2020 |
| 93 | 17701A04D1 | SRAVANA LAKSHMI BADIGIREDDY | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04D1 | SRAVANA LAKSHIVII BADIGIREDDY | | 12.02.2020 |
| 94 | 17701A04D2 | SREEKANTH KALLURU | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04D2 | SKELKANTI KALLOKO | | 12.02.2020 |
| 95 | 17701A04D3 | SREENIVASULU BODICHARLLA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04D5 | SREENIVASOLO BODICHARLLA | | 12.02.2020 |
| 96 | 17701A04D4 | SREEYA SALAPALA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04D4 | SILL TA SALAFALA | | 12.02.2020 |
| 97 | 17701A04D5 | SRIHITHA CHINNA RAMAPPAGARI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04D5 | SKINT NA CHINNA KAMAPPAGAKI | | 12.02.2020 |
| 98 | 17701A04D6 | SRIKANTH ACHARI KAMMARI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04D0 | SKIKANTT ACHARTRAMIMARI | | 12.02.2020 |
| 99 | 17701A04D7 | SUBRAMANYAM RAJU SANGARAJU | Advanced VLSI Concepts | 10.02.2020 TO |
| | 1//01A04D/ | SOBRAINAINTAINI RAJO SAINGARAJO | | 12.02.2020 |
| 100 | 17701A04D8 | SUCHARITHA MEEGADA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04D8 | SOCHANITIA MEEGADA | | 12.02.2020 |
| 101 | 17701A04D9 | SUDHAKAR SANA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04D9 | SUDHARAR SANA | | 12.02.2020 |
| 102 | 17701A04E0 | SUMANTH BHEEMA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04E0 | SOMANTH BHEEMA | | 12.02.2020 |
| 103 | 17701A04E1 | SUNEETHA CHINNARAMANNAGARI | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17701A04E1 | SONEETHA CHINNARAMANNAGARI | | 12.02.2020 |
| 104 | 1770040401 | | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17709A0401 | ABDUL SADIQ SHAIK | | 12.02.2020 |
| 105 | 1770040402 | ALTHAF SHAIK | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17709A0402 | ALITAR SHAIK | | 12.02.2020 |
| 106 | 1770040402 | | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17709A0403 | ANIL VIJAYANAGARAM | | 12.02.2020 |

| 107 | 17709A0404 | ASHRIT KUMAR SAHU | Advanced VLSI Concepts | 10.02.2020 TO |
|-----|--------------|---------------------------------|----------------------------|---------------|
| | 17705700101 | | | 12.02.2020 |
| 108 | 17709A0405 | CHINNI KRISHNA MADDINA | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17705A0405 | | | 12.02.2020 |
| 109 | 17700 10 100 | | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17709A0406 | GEETHA POLI | | 12.02.2020 |
| 110 | | | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17709A0407 | HARITHA PAPPIREDDY | | 12.02.2020 |
| 111 | | | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17709A0408 | HARSHA VARDHAN REDDY DYVAM | | 12.02.2020 |
| 112 | | | Advanced VLSI Concepts | 10.02.2020 TO |
| | 17709A0409 | HARSHITHA LALAPETA | | 12.02.2020 |
| | | N/ D Task | | 12.02.2020 |
| | | IV B.Tech | | |
| 1 | 16701A0404 | AKHILA GUTTA | Advances in Digital Signal | 03.02.2020 to |
| | 10701A0404 | | Processing | 05.02.2020 |
| 2 | 16701A0406 | AMARNATH REDDY BODELA | Advances in Digital Signal | 03.02.2020 to |
| | 1070170400 | | Processing | 05.02.2020 |
| 3 | 16701A0407 | AMEEN SUHAIL MS | Advances in Digital Signal | 03.02.2020 to |
| | 10/01/10/07 | | Processing | 05.02.2020 |
| 4 | 16701A0408 | ANANTH KUMAR KARNAM BALAIAHGARI | Advances in Digital Signal | 03.02.2020 to |
| | 10701710100 | | Processing | 05.02.2020 |
| 5 | 16701A0409 | ANIL CHINTALA | Advances in Digital Signal | 03.02.2020 to |
| | | | Processing | 05.02.2020 |
| 6 | 16701A0410 | ANIL KUMAR MADINENI | Advances in Digital Signal | 03.02.2020 to |
| | | | Processing | 05.02.2020 |
| 7 | 16701A0411 | ARAVINDANATH REDDY KALAGUTLA | Advances in Digital Signal | 03.02.2020 to |
| | | | Processing | 05.02.2020 |
| 8 | 16701A0412 | ARSHIA SHAIK | Advances in Digital Signal | 03.02.2020 to |
| | - | | Processing | 05.02.2020 |
| 9 | 16701A0413 | ASHOK KUMAR KAVALI | Advances in Digital Signal | 03.02.2020 to |
| | | | Processing | 05.02.2020 |

| 10 | 16701A0414 | BALA MANIKANTA BANDARU | Advances in Digital Signal | 03.02.2020 to |
|----|---------------|---------------------------------------|----------------------------|---------------|
| | 10701A0414 | | Processing | 05.02.2020 |
| 11 | 11 1670140445 | BHARATH KUMAR MEKALA | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0415 | | Processing | 05.02.2020 |
| 12 | 16701A0416 | BHARATH REDDY SANEPALLE | Advances in Digital Signal | 03.02.2020 to |
| | 10/01/0410 | | Processing | 05.02.2020 |
| 13 | 16701A0417 | BHARATHKUMAR REDDY MOLAKATHALLA | Advances in Digital Signal | 03.02.2020 to |
| | 10/01A041/ | BHARATHKOWAR REDDT WOLARATHALLA | Processing | 05.02.2020 |
| 14 | 1670140419 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0418 | BHARGHAVA GANDHAM | Processing | 05.02.2020 |
| 15 | 1070140410 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0419 | CHAITANYA LAHARI BHUMIREDDY | Processing | 05.02.2020 |
| 16 | 1070140420 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0420 | CHAITHANYA RANGA SAI POLA | Processing | 05.02.2020 |
| 17 | 1070140401 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0421 | CHANDANA GAJULAPALLE | Processing | 05.02.2020 |
| 18 | 1670140422 | 2 CHARITHA REDDY GABBIREDDY | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0422 | | Processing | 05.02.2020 |
| 19 | 1670140402 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0423 | CHELISHA BUCHAKKAGARI | Processing | 05.02.2020 |
| 20 | 20 1670140424 | CHENNA KRISHNA REDDY GAJJALA | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0424 | CHENNA KRISHNA REDDY GAJJALA | Processing | 05.02.2020 |
| 21 | 1070140425 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0425 | CHINNA GONGATI | Processing | 05.02.2020 |
| 22 | 1070100400 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0426 | CHITTEMMA JOHARAPURAM | Processing | 05.02.2020 |
| 23 | 1670140407 | | Advances in Digital Signal | 03.02.2020 to |
| | 10/01A0427 | 16701A0427DAMODHAR REDDY SINGAM REDDY | Processing | 05.02.2020 |
| 24 | 1670140420 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0428 | DEEPTHIKIRAN MADAKA | Processing | 05.02.2020 |
| 25 | 1670140420 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0429 | DHARANI REDDY PONNATHOTA | Processing | 05.02.2020 |
| 26 | 1670140420 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0430 | DIVYA RAJESWARI PAMULA | Processing | 05.02.2020 |

| 27 | 16701A0431 | DIVYA VAMULA | Advances in Digital Signal | 03.02.2020 to |
|----|-------------------|-------------------------------|----------------------------|---------------|
| | 10701A0451 | DIVIA VANIOLA | Processing | 05.02.2020 |
| 28 | 8 1670140422 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0432 | DIVYASREE CHILAMAKURI | Processing | 05.02.2020 |
| 29 | 16701A0433 | DIWAKAR SUNDARI | Advances in Digital Signal | 03.02.2020 to |
| | 10/01/0422 | DIWARAN SONDAN | Processing | 05.02.2020 |
| 30 | 16701A0434 | DURGA PRASAD REDDY BANDI | Advances in Digital Signal | 03.02.2020 to |
| | 10701A0454 | DUNGA PRASAD REDDI BANDI | Processing | 05.02.2020 |
| 31 | 1670140425 | DUSHYANTH REDDY GAADI | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0435 | DUSHTANTH REDDT GAADI | Processing | 05.02.2020 |
| 32 | 1670140426 | GAYATHRI AVULA YERRAMAREDDY | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0436 | GATATIAN AVOLA TERRAIVIAREDDT | Processing | 05.02.2020 |
| 33 | 1 (701) 0 / 27 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0437 | GURU SIRISHA MULLINTI | Processing | 05.02.2020 |
| 34 | 1 (701 4 0 4 2 9 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0438 | HARI SIVA PRASAD GANIGIPENTA | Processing | 05.02.2020 |
| 35 | 1670140420 | HARISHA BANDARU | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0439 | | Processing | 05.02.2020 |
| 36 | 16701A0461 | MOHAMMED ISLAM SHAIK | Advances in Digital Signal | 03.02.2020 to |
| | 10/01A0401 | | Processing | 05.02.2020 |
| 37 | 16701A0462 | MOHAN AKKINAGUNTLA | Advances in Digital Signal | 03.02.2020 to |
| | 10701A0402 | | Processing | 05.02.2020 |
| 38 | 1070140402 | 463 MOUNIKA DONTHYREDDY | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0463 | | Processing | 05.02.2020 |
| 39 | 16701 00464 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0464 | MOUNIKA GIDDALURU | Processing | 05.02.2020 |
| 40 | 1670140465 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0465 | 65 NARAHARI GIDDALURU | Processing | 05.02.2020 |
| 41 | 1670140466 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0466 | NARASIMHA REDDY KATHA | Processing | 05.02.2020 |
| 42 | 1670140467 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0467 | NARAYANA ALURI | Processing | 05.02.2020 |
| 43 | 16701A0468 | NAVEEN KUMAR KAMINI | Advances in Digital Signal | 03.02.2020 to |
| | 10/0140408 | | Processing | 05.02.2020 |

| 44 | 16701A0469 | NAVEEN KUMAR REDDY SANGANA | Advances in Digital Signal | 03.02.2020 to |
|----|---------------|--------------------------------|----------------------------|---------------|
| | 10/01/0405 | | Processing | 05.02.2020 |
| 45 | 45 16701A0470 | NEERAJAKSHI CHINTHA | Advances in Digital Signal | 03.02.2020 to |
| | 10/01/04/0 | NEERAJAKSHI CHINTHA | Processing | 05.02.2020 |
| 46 | 16701A0471 | NIHARIKA PEMMANA | Advances in Digital Signal | 03.02.2020 to |
| | 10/01/04/1 | | Processing | 05.02.2020 |
| 47 | 16701A0472 | PAVAN REDDY PALAKOLANU | Advances in Digital Signal | 03.02.2020 to |
| | 10/01A04/2 | PAVAN REDDI PALAKOLANO | Processing | 05.02.2020 |
| 48 | 16701A0473 | PAVANKUMAR NAIDU CHITRAJU | Advances in Digital Signal | 03.02.2020 to |
| | 10/01A04/5 | PAVANKOWAR NAIDU CHITRAJU | Processing | 05.02.2020 |
| 49 | 16701A0474 | PAVANKUMAR RAJU RAGHAVARAJU | Advances in Digital Signal | 03.02.2020 to |
| | 10/01A04/4 | PAVAINKUIVIAK KAJU KAGHAVAKAJU | Processing | 05.02.2020 |
| 50 | 16701A0475 | PAVITHRA PALA | Advances in Digital Signal | 03.02.2020 to |
| | 10/01A04/5 | PAVITIRA PALA | Processing | 05.02.2020 |
| 51 | 1670140476 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A0476 | PENCHALA PRAVEEN BADABHAGNI | Processing | 05.02.2020 |
| 52 | 16701A04A3 | SAI SADHANA HANUMANTHAKARI | Advances in Digital Signal | 03.02.2020 to |
| | 10/01A04A3 | | Processing | 05.02.2020 |
| 53 | 16701A04A4 | SAI SIVARAM NAGENDLA | Advances in Digital Signal | 03.02.2020 to |
| | 10701A04A4 | | Processing | 05.02.2020 |
| 54 | 54 16701A04A5 | SAI VIGNESH R | Advances in Digital Signal | 03.02.2020 to |
| | 10/01A04A5 | SAI VIGINESH K | Processing | 05.02.2020 |
| 55 | 1670140446 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A04A6 | SAITEJA DUGGINENI | Processing | 05.02.2020 |
| 56 | | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A04A7 | SANDHYA RANI SYAMANABOYANA | Processing | 05.02.2020 |
| 57 | 16701A04A8 | SANJEEVULU PODAGANDLA | Advances in Digital Signal | 03.02.2020 to |
| | 10/01A04A8 | SANJEEVOLO PODAGANDLA | Processing | 05.02.2020 |
| 58 | 16701A04A9 | | Advances in Digital Signal | 03.02.2020 to |
| | 10/01A04A9 | SANTHOSH NAIK RAMAVATH | Processing | 05.02.2020 |
| 59 | 4670440400 | 6701A04B0 SATEESH KUMAR SOLIGA | Advances in Digital Signal | 03.02.2020 to |
| | 10/01A0460 | | Processing | 05.02.2020 |
| 60 | 16701A04B1 | SATHEESH KUMAR SANDULO | Advances in Digital Signal | 03.02.2020 to |
| | 10/0140481 | SATILESI KUIVIAK SAINDULU | Processing | 05.02.2020 |

| 61 | 16701A04B2 | SATISH TALARI | Advances in Digital Signal | 03.02.2020 to |
|----|-------------|--------------------------------|----------------------------|---------------|
| | 10701A0462 | SATISH TALANI | Processing | 05.02.2020 |
| 62 | 62 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A04B4 | SHAMEERBASHA DUDEKULA | Processing | 05.02.2020 |
| 63 | 16701A04B5 | SISINDRINATH REDDY THAVISHELA | Advances in Digital Signal | 03.02.2020 to |
| | 10/01/04/05 | SISINDRINATTI REDUT THAVISHELA | Processing | 05.02.2020 |
| 64 | 16701A04B6 | SIVA KRISHNA VALLEPU | Advances in Digital Signal | 03.02.2020 to |
| | 10701A04B0 | SIVA KRISHINA VALLEFU | Processing | 05.02.2020 |
| 65 | | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A04B7 | SIVA PRASAD REDDY BANDI | Processing | 05.02.2020 |
| 66 | 1070140400 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A04B8 | SIVA REDDY NALLACHIGARI | Processing | 05.02.2020 |
| 67 | 1070140400 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A04B9 | SIVASANKAR NAIK BUKKE | Processing | 05.02.2020 |
| 68 | 1070140401 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A04C1 | SONIA KADAPA | Processing | 05.02.2020 |
| 69 | 1070140402 | SREENIVASULU NALLAMARU | Advances in Digital Signal | 03.02.2020 to |
| | 16701A04C2 | | Processing | 05.02.2020 |
| 70 | 1670140402 | SRIKANTH REDDY DEVIREDDY | Advances in Digital Signal | 03.02.2020 to |
| | 16701A04C3 | | Processing | 05.02.2020 |
| 71 | 1670140464 | SUCHITHRA UPPU | Advances in Digital Signal | 03.02.2020 to |
| | 16701A04C4 | SUCHITHKA UPPU | Processing | 05.02.2020 |
| 72 | 1070140405 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A04C5 | SUHAIL SHAIK | Processing | 05.02.2020 |
| 73 | | SUJA OBILI | Advances in Digital Signal | 03.02.2020 to |
| | 16701A04C6 | SOTA OBILI | Processing | 05.02.2020 |
| 74 | 1670140407 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A04C7 | SUMANTH GUDISE | Processing | 05.02.2020 |
| 75 | 1670140409 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A04C8 | SUPRAJA ELECHARALA | Processing | 05.02.2020 |
| 76 | 1670140400 | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A04C9 | SUPRIYA BHUPALAM | Processing | 05.02.2020 |
| 77 | | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A04D0 | SUPRIYA SAAREDDY | Processing | 05.02.2020 |

| 78 | 16701A04F0 | VENKATESWARLU KAMMANETHI | Advances in Digital Signal | 03.02.2020 to |
|----|--------------------------|------------------------------------|----------------------------|---------------|
| | 10/01/04/0 | | Processing | 05.02.2020 |
| 79 | ⁷⁹ 16701A04F2 | VISHNU SAI GUTTY | Advances in Digital Signal | 03.02.2020 to |
| | 10/01A04FZ | VISHINO SALGUTTE | Processing | 05.02.2020 |
| 80 | 16701A04F3 | VISHNU TEJA NALLA | Advances in Digital Signal | 03.02.2020 to |
| | 10/01/04/ 3 | | Processing | 05.02.2020 |
| 81 | 16701A04F4 | VISHNUVARDHINI PASUPULETI | Advances in Digital Signal | 03.02.2020 to |
| | 10/01A04F4 | VISHNOVARDHINI PASOFOLETI | Processing | 05.02.2020 |
| 82 | 16701A04F5 | VISHWAJA AKEPATI | Advances in Digital Signal | 03.02.2020 to |
| | 10701A04F3 | | Processing | 05.02.2020 |
| 83 | 16701A04F6 | VISHWAJA REDDY B | Advances in Digital Signal | 03.02.2020 to |
| | 10/01A04F0 | | Processing | 05.02.2020 |
| 84 | | | Advances in Digital Signal | 03.02.2020 to |
| | 16701A04F7 | VISWANATH CHALAMALA | Processing | 05.02.2020 |
| 85 | 16701A04F8 | YOUNUS HUSSAIN SHAIK | Advances in Digital Signal | 03.02.2020 to |
| | 10/01A04Fo | TOUNUS HUSSAIN SHAIK | Processing | 05.02.2020 |
| 86 | 17705A0408 | VARADA REDDY KUNDLA | Advances in Digital Signal | 03.02.2020 to |
| | 17705A0408 | | Processing | 05.02.2020 |
| 87 | 16709A0408 | HIMABINDU POOLAMALLIREDDY | Advances in Digital Signal | 03.02.2020 to |
| | 10709A0408 | | Processing | 05.02.2020 |
| 88 | 88 16709A0409 | KARTHIK CHOWDARY MASINA | Advances in Digital Signal | 03.02.2020 to |
| | 10709A0409 | KARTHIK CHOWDART MASINA | Processing | 05.02.2020 |
| 89 | 16709A0410 | KUMAR TEJA ANANDALA | Advances in Digital Signal | 03.02.2020 to |
| | 10709A0410 | KUMAR TEJA ANANDALA | Processing | 05.02.2020 |
| 90 | 16700 10 11 1 | KUSUMA ALIDENA | Advances in Digital Signal | 03.02.2020 to |
| | 16709A0411 | KUSUMA ALIDENA | Processing | 05.02.2020 |
| 91 | 16709A0412 | LAKSHMI NARASIMHA SWAMY PABBISETTI | Advances in Digital Signal | 03.02.2020 to |
| | 10709A0412 | | Processing | 05.02.2020 |
| 92 | 16709A0413 | 3 LAKSHMI VARAPRASAD CHALLA | Advances in Digital Signal | 03.02.2020 to |
| | 10/03/0415 | | Processing | 05.02.2020 |
| 93 | 16709A0415 | MADHUKAR MADALLAPALLE | Advances in Digital Signal | 03.02.2020 to |
| | 10/03/0413 | | Processing | 05.02.2020 |
| 94 | 16709A0416 | MAHESWARI DESU | Advances in Digital Signal | 03.02.2020 to |
| | 10703A0410 | | Processing | 05.02.2020 |

| 95 | 16709A0417 | MALLESWAR REDDY GAJJALA | Advances in Digital Signal | 03.02.2020 to |
|----|------------|----------------------------|----------------------------|---------------|
| | 10709A0417 | MALLESWAR REDDT GAJJALA | Processing | 05.02.2020 |
| 96 | 1670040419 | MALLIKARJUN BOMMU | Advances in Digital Signal | 03.02.2020 to |
| | 16709A0418 | | Processing | 05.02.2020 |
| 97 | 16709A0419 | MANIKANTA REDDY DUMPALA | Advances in Digital Signal | 03.02.2020 to |
| | 10709A0419 | MANIKANTA REDDT DOMPALA | Processing | 05.02.2020 |
| 98 | 16709A0420 | NAVEEN KUMAR REDDY BANDI | Advances in Digital Signal | 03.02.2020 to |
| | 10705A0420 | NAVEEN KUIVIAR REDDT BANDI | Processing | 05.02.2020 |

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This is to certify that Mr/Ms. <u>AMARANATH CHANAGANI</u> bearing H.T.No.<u>17701A0403</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>ANDRO MARTIN VIVEK S R</u> bearing H.T.No.<u>17701A0405</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>ASMA SYED</u> bearing H.T.No.<u>17701A0406</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019</u> <u>to 17.09.2019</u>.

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This is to certify that Mr/Ms. **BABA SAI KISHORE GADARAPU** bearing H.T.No.<u>17701A0407</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>BASHA SHAIK</u> bearing H.T.No.<u>17701A0408</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019</u> <u>to 17.09.2019</u>.

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This is to certify that Mr/Ms. **BHARATH KATIKELA** bearing H.T.No. **17701A0409** has attended a Value Added Course on "ASSERTION BASED VLSI DESIGN" organized by department of ECE during 16.09.2019 to 17.09.2019.

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This is to certify that Mr/Ms. <u>BHARATH KUMAR NAKKA</u> bearing H.T.No.<u>17701A0410</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>BHARATHKUMARREDDY RAJUPALLI</u> bearing H.T.No.<u>17701A0411</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>BHARGAV REDDY DUMPA</u> bearing H.T.No.<u>17701A0412</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>BHAVYA SREE OGGU</u> bearing H.T.No.<u>17701A0413</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>CHAKRADHAR RAJU PANDETI</u> bearing H.T.No.<u>17701A0414</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>CHANDRAKALAVATHI KATREDDY</u> bearing H.T.No.<u>17701A0416</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>CHANDRASEKHAR NAGASWARAM</u> bearing H.T.No.<u>17701A0417</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>CHARAN KUMAR PAGADALA</u> bearing H.T.No.<u>17701A0418</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>CHARANKUMAR REDDY LINGALA</u> bearing H.T.No.<u>17701A0419</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>CHARANTEJA POOLA</u> bearing H.T.No.<u>17701A0420</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>DEEPA POLAMREDDY</u> bearing H.T.No.<u>17701A0421</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. **<u>DHANUSHA THOTA</u>** bearing H.T.No.**<u>17701A0422</u>** has attended a Value Added Course on "ASSERTION BASED VLSI DESIGN" organized by department of ECE during 16.09.2019 to 17.09.2019.

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This is to certify that Mr/Ms. **<u>DHARANI ANANDI</u>** bearing H.T.No.<u>17701A0423</u> has attended a Value Added Course on "ASSERTION BASED VLSI DESIGN" organized by department of ECE during 16.09.2019 to 17.09.2019.

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This is to certify that Mr/Ms. <u>DHARANI BRAMHADEVUNI</u> bearing H.T.No.<u>17701A0424</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>DIVYA ANDRA</u> bearing H.T.No.<u>17701A0425</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019</u> <u>to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>DIVYA KONETI</u> bearing H.T.No.<u>17701A0426</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019</u> <u>to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>DIVYANJALI GALISETTY</u> bearing H.T.No.<u>17701A0427</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. **DOSHNA KOUSALYA ARIGELA** bearing H.T.No.<u>17701A0428</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. **DURGA VARA PRASAD BANDI** bearing H.T.No.<u>17701A0429</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>GANGADHAR MANIKALA</u> bearing H.T.No.<u>17701A0430</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>GAYATHRI KAKARLA</u> bearing H.T.No.<u>17701A0431</u> has attended a Value Added Course on "ASSERTION BASED VLSI DESIGN" organized by department of ECE during 16.09.2019 to 17.09.2019.

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This is to certify that Mr/Ms. <u>GIREESH KUMAR DEVARAPALLI</u> bearing H.T.No.<u>17701A0432</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>GOVARDHAN MALISETTY</u> bearing H.T.No.<u>17701A0433</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>GURU KISHORE CHITTIBOINA</u> bearing H.T.No.<u>17701A0434</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>GURU PAVANKALYAN UPPALURU</u> bearing H.T.No.<u>17701A0435</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>GURU SHEKHAR PUPPAM</u> bearing H.T.No.<u>17701A0436</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>GURUNAVEEN BAPANAPALLE</u> bearing H.T.No.<u>17701A0437</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>HARIKA ANUGONDA</u> bearing H.T.No.<u>17701A0438</u> has attended a Value Added Course on "ASSERTION BASED VLSI DESIGN" organized by department of ECE during 16.09.2019 to 17.09.2019.

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This is to certify that Mr/Ms. <u>HARSHITHA REDDY METTUPALLE</u> bearing H.T.No.<u>17701A0439</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>HASEENA BEGAM SHAIK</u> bearing H.T.No.<u>17701A0440</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>HIMABINDU M</u> bearing H.T.No.<u>17701A0441</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019</u> <u>to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>HIMABINDU UMMADIREDDY</u> bearing H.T.No.<u>17701A0442</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>HIMAJA KOKKANTI</u> bearing H.T.No.<u>17701A0443</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. JAGRUTHI PEYYALA bearing H.T.No.17701A0444 has attended a Value Added Course on "ASSERTION BASED VLSI DESIGN" organized by department of ECE during 16.09.2019 to 17.09.2019.

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This is to certify that Mr/Ms. JAYA BHARATH REDDY MANDADI bearing H.T.No.<u>17701A0445</u> has attended a Value Added Course on "ASSERTION BASED VLSI DESIGN" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>JYOTHI GANJIKUNTA</u> bearing H.T.No.<u>17701A0446</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>KARISHMA SHAIK</u> bearing H.T.No.<u>17701A0447</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>KAVERI MALLIREDDY</u> bearing H.T.No.<u>17701A0448</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>KEERTHANA RAMANNA GARI</u> bearing H.T.No.<u>17701A0449</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>KHADEEJ UL UMERAH BEPAR</u> bearing H.T.No.<u>17701A0450</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>KRANTHI CHEERALA</u> bearing H.T.No.<u>17701A0451</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>LAHARI SATTU</u> bearing H.T.No.<u>17701A0452</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019</u> <u>to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>LAKSHMI JUTURU</u> bearing H.T.No.<u>17701A0453</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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Head of the Department



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This is to certify that Mr/Ms. **LAKSHMI KANTHAMMA MANGALI** bearing H.T.No.<u>17701A0454</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. *LAKSHMI PRASANNA KARIMEDDALA* bearing H.T.No.<u>17701A0455</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>LAKSHMI PRASANNA NARLA</u> bearing H.T.No.<u>17701A0456</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>MALLIKARJUNA BAPANAPALLI</u> bearing H.T.No.<u>17701A0464</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>MAMATHA BUSIREDDY</u> bearing H.T.No.<u>17701A0465</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>MANIKANTA MEDIKONDU</u> bearing H.T.No.<u>17701A0466</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>MEGHANA GANDI</u> bearing H.T.No.<u>17701A0467</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>MEGHANA GUTHA</u> bearing H.T.No.<u>17701A0468</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>NAADIYA SHAIK</u> bearing H.T.No.<u>17701A0469</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>NAGA JYOTHI UMMADIREDDY</u> bearing H.T.No.<u>17701A0470</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>NARASIMHULU CHINTHAMANI</u> bearing H.T.No.<u>17701A0471</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>NAVEEN AREPALLI</u> bearing H.T.No.<u>17701A0472</u> has attended a Value Added Course on "ASSERTION BASED VLSI DESIGN" organized by department of ECE during 16.09.2019 to 17.09.2019.

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This is to certify that Mr/Ms. <u>NAVEEN KALLURU</u> bearing H.T.No.<u>17701A0473</u> has attended a Value Added Course on "ASSERTION BASED VLSI DESIGN" organized by department of ECE during 16.09.2019 to 17.09.2019.

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This is to certify that Mr/Ms. <u>NAVEEN KUMAR REDDY GANDLURU</u> bearing H.T.No.<u>17701A0474</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>NAVEEN KUMAR REDDY GOPIREDDY</u> bearing H.T.No.<u>17701A0475</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>NAVEEN KUMAR SIDDAM</u> bearing H.T.No.<u>17701A0476</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>NAVYA DANASI</u> bearing H.T.No.<u>17701A0477</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>NAVYA LAKSHMI DONTHU</u> bearing H.T.No.<u>17701A0478</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>NAYAB RASOOL SHAIK</u> bearing H.T.No.<u>17701A0479</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>NEELIMA NAGARURI</u> bearing H.T.No.<u>17701A0480</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>NIHARIKA KANDUKURI</u> bearing H.T.No.<u>17701A0481</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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CERTIFICATE OF APPRECIATION

This is to certify that Mr/Ms. <u>NIKHIL DASARI</u> bearing H.T.No.<u>17701A0482</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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CERTIFICATE OF APPRECIATION

This is to certify that Mr/Ms. <u>NIKHIL KENCHUGUNDU</u> bearing H.T.No.<u>17701A0483</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>NIRVISHA GANGIREDDY</u> bearing H.T.No.<u>17701A0484</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>NITESH KUMAR AMINENI</u> bearing H.T.No.<u>17701A0485</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>OBULREDDY GODI</u> bearing H.T.No.<u>17701A0486</u> has attended a Value Added Course on "ASSERTION BASED VLSI DESIGN" organized by department of ECE during 16.09.2019 to 17.09.2019.

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This is to certify that Mr/Ms. <u>PAAVANA KUMAR CHINTALAPALLI</u> bearing H.T.No.<u>17701A0487</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>PAVAN KALYAN MAJJIGA</u> bearing H.T.No.<u>17701A0488</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>PAVANTEJA REDDY GADI</u> bearing H.T.No.<u>17701A0489</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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CERTIFICATE OF APPRECIATION

This is to certify that Mr/Ms. <u>PRADEEP REDDY SAREDDY</u> bearing H.T.No.<u>17701A0490</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>**PRASANNA NAGENDRA BANNEL</u>** bearing H.T.No.<u>17701A0491</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.</u>

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This is to certify that Mr/Ms. **PRAVEEN KUMAR KAMALAPURAM** bearing H.T.No.<u>17701A0492</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>**PRAVEEN KUMAR REDDY DUGGIREDDY</u>** bearing H.T.No.<u>17701A0493</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.</u>

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This is to certify that Mr/Ms. **PRAVEENA KODURU** bearing H.T.No.**<u>17701A0494</u>** has attended a Value Added Course on "ASSERTION BASED VLSI DESIGN" organized by department of ECE during 16.09.2019 to 17.09.2019.

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This is to certify that Mr/Ms. **SUBRAMANYAM RAJU SANGARAJU** bearing H.T.No.<u>17701A04D7</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>SUCHARITHA MEEGADA</u> bearing H.T.No.<u>17701A04D8</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>SUDHAKAR SANA</u> bearing H.T.No.<u>17701A04D9</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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CERTIFICATE OF APPRECIATION

This is to certify that Mr/Ms. <u>SUMANTH BHEEMA</u> bearing H.T.No.<u>17701A04E0</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>SUNEETHA CHINNARAMANNAGARI</u> bearing H.T.No.<u>17701A04E1</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>SUPRAJA KOLLU</u> bearing H.T.No.<u>17701A04E2</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>SURENDRABABU VANNEM</u> bearing H.T.No.<u>17701A04E3</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>SURESH KUMAR REDDY DESIREDDY</u> bearing H.T.No.<u>17701A04E4</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>SWAROOP KUMAR SAILESWARAM</u> bearing H.T.No.<u>17701A04E5</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>SWATHI DERANGULA</u> bearing H.T.No.<u>17701A04E6</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>**TEJASWINI YANAMALA</u>** bearing H.T.No.<u>17701A04E7</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.</u>

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This is to certify that Mr/Ms. <u>MANISHA SOMALA</u> bearing H.T.No.<u>17709A0413</u> has attended a Value Added Course on "ASSERTION BASED VLSI DESIGN" organized by department of ECE during 16.09.2019 to 17.09.2019.

VAC Co-ordinator

Head of the Department



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CERTIFICATE OF APPRECIATION

This is to certify that Mr/Ms. <u>MANJULA BELLALA</u> bearing H.T.No.<u>17709A0414</u> has attended a Value Added Course on "ASSERTION BASED VLSI DESIGN" organized by department of ECE during 16.09.2019 to 17.09.2019.

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This is to certify that Mr/Ms. <u>MOHITHA SAI DUDDUKUNTA</u> bearing H.T.No.<u>17709A0415</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>MUNI LALITH KUMAR VOMMI</u> bearing H.T.No.<u>17709A0416</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>MUNI PREM KUMAR VOMMI</u> bearing H.T.No.<u>17709A0417</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>NAVEEN VENKATASAI BANALA</u> bearing H.T.No.<u>17709A0418</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. **PRASANNA LINGAM** bearing H.T.No. **17709A0419** has attended a Value Added Course on "ASSERTION BASED VLSI DESIGN" organized by department of ECE during 16.09.2019 to 17.09.2019.

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This is to certify that Mr/Ms. <u>PRATHYUSHA SOMALA</u> bearing H.T.No.<u>17709A0421</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>PRAVALLIKA MUMMADI</u> bearing H.T.No.<u>17709A0422</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>PREETHI POCHAMIREDDY</u> bearing H.T.No.<u>17709A0423</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. **PREMKUMAR REDDY MUMMADI** bearing H.T.No.<u>17709A0424</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>**RANI BOLLA</u>** bearing H.T.No.<u>17709A0425</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019</u> <u>to 17.09.2019</u>.</u>

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This is to certify that Mr/Ms. <u>**REDDY BHANU RAMI REDDY GARI</u> bearing H.T.No.<u>17709A0426</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.</u>**



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This is to certify that Mr/Ms. <u>SAI KUMAR MODAGALA</u> bearing H.T.No.<u>17709A0427</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>SAILESHNANDAN REDDY VADDI</u> bearing H.T.No.<u>17709A0428</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>SAINATH MANDLI</u> bearing H.T.No.<u>17709A0429</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>SHAHEENA SHAIK</u> bearing H.T.No.<u>17709A0430</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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This is to certify that Mr/Ms. <u>SREEVATSAV REDDY GAJJALA</u> bearing H.T.No.<u>17709A0432</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.

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CERTIFICATE OF APPRECIATION

This is to certify that Mr/Ms. <u>SRI HARSHINI GURRAMKONDA</u> bearing H.T.No.<u>17709A0433</u> has attended a Value Added Course on "ASSERTION BASED VLSI DESIGN" organized by department of ECE during 16.09.2019 to 17.09.2019.

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This is to certify that Mr/Ms. **TEJASREE RACHURI** bearing H.T.No. **17709A0434** has attended a Value Added Course on "ASSERTION BASED VLSI DESIGN" organized by department of ECE during 16.09.2019 to 17.09.2019.

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This is to certify that Mr/Ms. <u>**TEJESH SANISETTI</u>** bearing H.T.No.<u>17709A0435</u> has attended a Value</u> Added Course on "ASSERTION BASED VLSI DESIGN" organized by department of ECE during 16.09.2019 to 17.09.2019.

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This is to certify that Mr/Ms. <u>THUSHARA BHARGAVI SINDHURI</u> bearing H.T.No.<u>17709A0436</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>TRINATH C</u> bearing H.T.No.<u>17709A0437</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019</u> <u>to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>VENKATA RANGA SARANYA EPURI</u> bearing H.T.No.<u>17709A0438</u> has attended a Value Added Course on "<u>ASSERTION BASED VLSI DESIGN</u>" organized by department of ECE during <u>16.09.2019 to 17.09.2019</u>.



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This is to certify that Mr/Ms. <u>ABHINAV KUMAR REDDY GADDAM</u> bearing H.T.No. <u>18701A0401</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>ADI LAKSHMI DEVI GAJJALA</u> bearing H.T.No. <u>18701A0402</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>AFRIN SHAIK</u> bearing H.T.No. <u>18701A0403</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>AKHILA CHAPPIDI</u> bearing H.T.No. <u>18701A0404</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

Head of the Department



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This is to certify that Mr/Ms. <u>AKHILA GOLLA</u> bearing H.T.No. <u>18701A0405</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>AKHILA SEELAM</u> bearing H.T.No. <u>18701A0406</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>ANIL KUMAR PALURU CHAKALI</u> bearing H.T.No. <u>18701A0407</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>ANIL KUMAR REDDY CHENCHIREDDY</u> bearing H.T.No. <u>18701A0408</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

Head of the Department



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This is to certify that Mr/Ms. <u>ANIL KUMAR REDDY KETHIREDDY</u> bearing H.T.No. <u>18701A0409</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>ANJALI VADLA</u> bearing H.T.No. <u>18701A0410</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>ANUSHA G</u> bearing H.T.No. <u>18701A0411</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

VAC Coordinator



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This is to certify that Mr/Ms. <u>ARCHANA SONTIMALI</u> bearing H.T.No. <u>18701A0412</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

Head of the Department



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This is to certify that Mr/Ms. <u>ARSHIA SHAJARIN SAYYAD</u> bearing H.T.No. <u>18701A0413</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

Head of the Department



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This is to certify that Mr/Ms. <u>ASHOK KUMAR REDDY THOTTI</u> bearing H.T.No. <u>18701A0414</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>ASIF SHAIK</u> bearing H.T.No. <u>18701A0415</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>ASLAM BASHA SHAIK</u> bearing H.T.No. <u>18701A0416</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>AVINASH GUNTURU</u> bearing H.T.No. <u>18701A0417</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>BANDHAVI DUBBOLLA</u> bearing H.T.No. <u>18701A0418</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>BHARGAVA REDDY MOLAKATHALLA</u> bearing H.T.No. <u>18701A0419</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>MAHESH AKULA</u> bearing H.T.No. <u>18701A0470</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>MAHESWAR REDDY AKEPATI</u> bearing H.T.No. <u>18701A0471</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>MAHESWARI CHILLA</u> bearing H.T.No. <u>18701A0472</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>MAHITHA YAGATEELA</u> bearing H.T.No. <u>18701A0473</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>MANASA TURAKA</u> bearing H.T.No. <u>18701A0474</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>MANISHA AYYAPUSETTY</u> bearing H.T.No. <u>18701A0475</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>MANOJ KUMAR PATIL</u> bearing H.T.No. <u>18701A0476</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>MARUTHI PRAVEEN SAGABALA</u> bearing H.T.No. <u>18701A0477</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>MOHAMMAD AKRAM SYED</u> bearing H.T.No. <u>18701A0478</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>MOHAMMAD ANAS SHAIK</u> bearing H.T.No. <u>18701A0479</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>MOHAMMAD SHAIK</u> bearing H.T.No. <u>18701A0480</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>SHYLU SUNKESULA</u> bearing H.T.No. <u>18701A04C4</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>SIREESHA NARAPUREDDY</u> bearing H.T.No. <u>18701A04C5</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>SIVA PRASAD MADANAPALLI</u> bearing H.T.No. <u>18701A04C6</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

Head of the Department



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This is to certify that Mr/Ms. <u>SIVA RAMA KRISHNA REDDY KARNATI</u> bearing H.T.No. <u>18701A04C7</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

Head of the Department



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This is to certify that Mr/Ms. <u>SIVA SREEKAR Y</u> bearing H.T.No. <u>18701A04C8</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>SIVARAM MALLELA</u> bearing H.T.No. <u>18701A04C9</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>SOMA SUNDAR REDDY YARAMAREDDY</u> bearing H.T.No. <u>18701A04D0</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>SOWJANYA KADIYALA</u> bearing H.T.No. <u>18701A04D1</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>SRAVANI MALLUGARI</u> bearing H.T.No. <u>18701A04D2</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>SRAVANI YALLATURU</u> bearing H.T.No. <u>18701A04D3</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>SREE LAKSHMI BHAVANASI</u> bearing H.T.No. <u>18701A04D4</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>SUDHA KONETI</u> bearing H.T.No. <u>18701A04E0</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>SUDHA RANI RANGA</u> bearing H.T.No. <u>18701A04E1</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>SUDHARSHAN KUMAR CHANDU</u> bearing H.T.No. <u>18701A04E2</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>SUDHARSHAN REDDY GALI</u> bearing H.T.No. <u>18701A04E3</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>SUDHEESHNADEVI MULA</u> bearing H.T.No. <u>18701A04E4</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>SUMANTH GUTAM</u> bearing H.T.No. <u>18701A04E5</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>SUMANTH KUMAR KARIDIBUSHIGARI</u> bearing H.T.No. <u>18701A04E6</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>SUMANTH NIMMALA</u> bearing H.T.No. <u>18701A04E7</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>SUPRAJA YERRAGORLA</u> bearing H.T.No. <u>18701A04E8</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>SUSHMA PANJAGALLA</u> bearing H.T.No. <u>18701A04E9</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>SUSMITHA GOVINDU</u> bearing H.T.No. <u>18701A04F0</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>SWATHI REDDY R</u> bearing H.T.No. <u>18701A04F1</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>THANUJA SRIPATI</u> bearing H.T.No. <u>18701A04F2</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>VENKATA SAI CHINNARI</u> bearing H.T.No. <u>18701A04G0</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>VENKATA THARUN REDDY GANDHAM</u> bearing H.T.No. <u>18701A04G1</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>VENU BHARGAV REDDY NOOKA</u> bearing H.T.No. <u>18701A04G2</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>VERONIKAMANI AMBOORU</u> bearing H.T.No. <u>18701A04G3</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>VINAY KUMAR REDDY MALLELA</u> bearing H.T.No. <u>18701A04G4</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>VINITHA PAPPIREDDY</u> bearing H.T.No. <u>18701A04G5</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>VISHALAKSHI KOMMA</u> bearing H.T.No. <u>18701A04G6</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>VISHNU PRIYA D</u> bearing H.T.No. <u>18701A04G7</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>VISHNU VARDHAN REDDY MALLU</u> bearing H.T.No. <u>18701A04G8</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>VIVEKA GANDAVARAPU</u> bearing H.T.No. <u>18701A04G9</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>VYSHNAVI ANKIREDDY PALLI</u> bearing H.T.No. <u>18701A04H0</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>BHARATH TEJA POTHALAM</u> bearing H.T.No. <u>18709A0406</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>BHAVYA SOMPALLI</u> bearing H.T.No. <u>18709A0407</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>DEEPA MOPURI</u> bearing H.T.No. <u>18709A0408</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>DEEPANJANI KODATALA</u> bearing H.T.No. <u>18709A0409</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>FAZULULLA SHAIK</u> bearing H.T.No. <u>18709A0410</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>GOURI PRIYA CHOWDAM VARAPAKU</u> bearing H.T.No. <u>18709A0411</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>HARSHA CHAITANYA YEDDALAPALLI</u> bearing H.T.No. <u>18709A0412</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>HEMANTH KUMAR REDDY DUGGIREDDY</u> bearing H.T.No. <u>18709A0413</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>HEY RAMA RAJU JAGADABHI</u> bearing H.T.No. <u>18709A0414</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>INDIVAR MADDINA</u> bearing H.T.No. <u>18709A0415</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>JALEELBASHA SAGINALA</u> bearing H.T.No. <u>18709A0416</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. JAYASREE CHUKKA bearing H.T.No. <u>18709A0417</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. JEEVAN REDDY AMBAVARAM bearing H.T.No. <u>18709A0418</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>KAVYA SREE MALIKIREDDY</u> bearing H.T.No. <u>18709A0419</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>LAKSHMI PRIYA MUKKAMALLA</u> bearing H.T.No. <u>18709A0420</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>MADHURI KARANAM</u> bearing H.T.No. <u>18709A0421</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>MAMATHA PEDDATHIMMAYYAGARI</u> bearing H.T.No. <u>18709A0422</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>MOULALI UDAYAGIRI</u> bearing H.T.No. <u>18709A0423</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>MUNIBHARGAV THOTA</u> bearing H.T.No. <u>18709A0424</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>NAVEEN KUMAR REDDY PALURU</u> bearing H.T.No. <u>18709A0425</u> has attended a Value Added Course on "<u>Basics for MATLAB programming</u>" organized by department of ECE during <u>13.08.2019 to 16.08.2019</u>.

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This is to certify that Mr/Ms. <u>ABDUL KALAM SHAIK</u> bearing H.T.No.<u>17701A0401</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>AMARANATH CHANAGANI</u> bearing H.T.No.<u>17701A0403</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>AMRUTHA SREE KORLAKUNTA</u> bearing H.T.No.<u>17701A0404</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>ANDRO MARTIN VIVEK S R</u> bearing H.T.No.<u>17701A0405</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>ASMA SYED</u> bearing H.T.No.<u>17701A0406</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>BABA SAI KISHORE GADARAPU</u> bearing H.T.No.<u>17701A0407</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>BASHA SHAIK</u> bearing H.T.No.<u>17701A0408</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>BHARATH KATIKELA</u> bearing H.T.No.<u>17701A0409</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>BHARATH KUMAR NAKKA</u> bearing H.T.No.<u>17701A0410</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>BHARATHKUMARREDDY RAJUPALLI</u> bearing H.T.No.<u>17701A0411</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>BHARGAV REDDY DUMPA</u> bearing H.T.No.<u>17701A0412</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>BHAVYA SREE OGGU</u> bearing H.T.No.<u>17701A0413</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>CHAKRADHAR RAJU PANDETI</u> bearing H.T.No.<u>17701A0414</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>CHANDRAKALA ATMAKUR</u> bearing H.T.No.<u>17701A0415</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>CHANDRAKALAVATHI KATREDDY</u> bearing H.T.No.<u>17701A0416</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>CHANDRASEKHAR NAGASWARAM</u> bearing H.T.No.<u>17701A0417</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>CHARAN KUMAR PAGADALA</u> bearing H.T.No.<u>17701A0418</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>CHARANKUMAR REDDY LINGALA</u> bearing H.T.No.<u>17701A0419</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>CHARANTEJA POOLA</u> bearing H.T.No.<u>17701A0420</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>DEEPA POLAMREDDY</u> bearing H.T.No.<u>17701A0421</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>DHANUSHA THOTA</u> bearing H.T.No.<u>17701A0422</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>DHARANI ANANDI</u> bearing H.T.No.<u>17701A0423</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>DHARANI BRAMHADEVUNI</u> bearing H.T.No.<u>17701A0424</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>DIVYA ANDRA</u> bearing H.T.No.<u>17701A0425</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>DIVYA KONETI</u> bearing H.T.No.<u>17701A0426</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>DIVYANJALI GALISETTY</u> bearing H.T.No.<u>17701A0427</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. **DOSHNA KOUSALYA ARIGELA** bearing H.T.No.<u>17701A0428</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>NAVEEN KUMAR REDDY GANDLURU</u> bearing H.T.No.<u>17701A0474</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>NAVEEN KUMAR REDDY GOPIREDDY</u> bearing H.T.No.<u>17701A0475</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>NAVEEN KUMAR SIDDAM</u> bearing H.T.No.<u>17701A0476</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>NAVYA DANASI</u> bearing H.T.No.<u>17701A0477</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>NAVYA LAKSHMI DONTHU</u> bearing H.T.No.<u>17701A0478</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>NAYAB RASOOL SHAIK</u> bearing H.T.No.<u>17701A0479</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>NEELIMA NAGARURI</u> bearing H.T.No.<u>17701A0480</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>NIHARIKA KANDUKURI</u> bearing H.T.No.<u>17701A0481</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>NIKHIL DASARI</u> bearing H.T.No.<u>17701A0482</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>NIKHIL KENCHUGUNDU</u> bearing H.T.No.<u>17701A0483</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>NIRVISHA GANGIREDDY</u> bearing H.T.No.<u>17701A0484</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>NITESH KUMAR AMINENI</u> bearing H.T.No.<u>17701A0485</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>OBULREDDY GODI</u> bearing H.T.No.<u>17701A0486</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>PAAVANA KUMAR CHINTALAPALLI</u> bearing H.T.No.<u>17701A0487</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>PAVAN KALYAN MAJJIGA</u> bearing H.T.No.<u>17701A0488</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>PAVANTEJA REDDY GADI</u> bearing H.T.No.<u>17701A0489</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>PRADEEP REDDY SAREDDY</u> bearing H.T.No.<u>17701A0490</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>PRASANNA NAGENDRA BANNEL</u> bearing H.T.No.<u>17701A0491</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>PRAVEEN KUMAR KAMALAPURAM</u> bearing H.T.No.<u>17701A0492</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>PRAVEEN KUMAR REDDY DUGGIREDDY</u> bearing H.T.No.<u>17701A0493</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>PRAVEENA KODURU</u> bearing H.T.No.<u>17701A0494</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>**RAAGA CHANDANA KASIREDDY</u>** bearing H.T.No.<u>17701A0495</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.</u>



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This is to certify that Mr/Ms. <u>**RAGHAVENDRA MAJJARI</u>** bearing H.T.No.<u>17701A0496</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.</u>



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This is to certify that Mr/Ms. <u>**RAJA GOLLA</u>** bearing H.T.No.<u>17701A0497</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.</u>



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This is to certify that Mr/Ms. <u>**RAJENDRA BANDI</u>** bearing H.T.No.<u>17701A0498</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.</u>



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This is to certify that Mr/Ms. <u>**RAJESWARI MULI</u>** bearing H.T.No.<u>17701A0499</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.</u>



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This is to certify that Mr/Ms. <u>**RAJU ASPARI</u>** bearing H.T.No.<u>17701A04A0</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.</u>



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This is to certify that Mr/Ms. <u>RAKESH NAIDU PAINENI</u> bearing H.T.No.<u>17701A04A1</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>RAMYA SREE SAMA</u> bearing H.T.No.<u>17701A04A2</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>**RASI NANDALURU</u>** bearing H.T.No.<u>17701A04A3</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.</u>



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This is to certify that Mr/Ms. <u>**ROHITHA V**</u> bearing H.T.No.<u>17701A04A4</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>**RUDRA VEENA THIMMA REDDY</u>** bearing H.T.No.<u>17701A04A5</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.</u>



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This is to certify that Mr/Ms. <u>SAI KRISHNA DANDALA</u> bearing H.T.No.<u>17701A04A6</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SAI KUMAR REDDY GAJJALA</u> bearing H.T.No.<u>17701A04A7</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SAI VARDHAN BOLLA</u> bearing H.T.No.<u>17701A04A8</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SAIKUMAR VAKKANTI</u> bearing H.T.No.<u>17701A04A9</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SAINADH GOPINENI</u> bearing H.T.No.<u>17701A04B0</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SAIUSHA RAMYA BADRI</u> bearing H.T.No.<u>17701A04B1</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>VENKATA SAI KRISHNA KUMAR SUNKU</u> bearing H.T.No.<u>16701A04E2</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>KAVYA KUMMARA</u> bearing H.T.No.<u>18705A0406</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>KAVYA SREE KURLA</u> bearing H.T.No.<u>18705A0407</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>KIRAN KUMAR SAPOLLA</u> bearing H.T.No.<u>18705A0408</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>MOHAMMAD IMRAN PATNOOL</u> bearing H.T.No.<u>18705A0409</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>NARENDRA GANDU</u> bearing H.T.No.<u>18705A0410</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> <u>to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>PRADEEP KUMAR AREMARATI</u> bearing H.T.No.<u>18705A0411</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>PRANAVI GUMMANA</u> bearing H.T.No.<u>18705A0412</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SANDEEP GONGATI</u> bearing H.T.No.<u>17701A04B2</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SANDEEP KALABAI</u> bearing H.T.No.<u>17701A04B3</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SANDHYARANI GUTTURU</u> bearing H.T.No.<u>17701A04B4</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SASIKUMAR EMMIDI</u> bearing H.T.No.<u>17701A04B5</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SATHISH KUMAR REDDY KALLURI</u> bearing H.T.No.<u>17701A04B6</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SATYA NARAYANA KAPPALA</u> bearing H.T.No.<u>17701A04B7</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SHANTHA BABU DONKAPPA</u> bearing H.T.No.<u>17701A04B8</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SIREESHA GALI</u> bearing H.T.No.<u>17701A04B9</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SIREESHA KASIREDDY</u> bearing H.T.No.<u>17701A04C0</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SIREESHA LANGANURU</u> bearing H.T.No.<u>17701A04C1</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SIVA JYOSHNA BANDI</u> bearing H.T.No.<u>17701A04C2</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SIVA KUMAR KOPPLA</u> bearing H.T.No.<u>17701A04C3</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SIVA PRASAD PURUM</u> bearing H.T.No.<u>17701A04C4</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SIVA PRIYA UYYALA</u> bearing H.T.No.<u>17701A04C5</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SIVA SANKAR MANCHALA</u> bearing H.T.No.<u>17701A04C6</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SOMA SUNDHAR VELPULA</u> bearing H.T.No.<u>17701A04C7</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SONI KATARI</u> bearing H.T.No.<u>17701A04C8</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SPOORTHI JINKA</u> bearing H.T.No.<u>17701A04C9</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> <u>to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SRAVAN KUMAR CHINTHAMANI</u> bearing H.T.No.<u>17701A04D0</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SRAVANA LAKSHMI BADIGIREDDY</u> bearing H.T.No.<u>17701A04D1</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SREEKANTH KALLURU</u> bearing H.T.No.<u>17701A04D2</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SREENIVASULU BODICHARLLA</u> bearing H.T.No.<u>17701A04D3</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SREEYA SALAPALA</u> bearing H.T.No.<u>17701A04D4</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SRIHITHA CHINNA RAMAPPAGARI</u> bearing H.T.No.<u>17701A04D5</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SRIKANTH ACHARI KAMMARI</u> bearing H.T.No.<u>17701A04D6</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SUBRAMANYAM RAJU SANGARAJU</u> bearing H.T.No.<u>17701A04D7</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SUCHARITHA MEEGADA</u> bearing H.T.No.<u>17701A04D8</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SUDHAKAR SANA</u> bearing H.T.No.<u>17701A04D9</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SUMANTH BHEEMA</u> bearing H.T.No.<u>17701A04E0</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>SUNEETHA CHINNARAMANNAGARI</u> bearing H.T.No.<u>17701A04E1</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>ABDUL SADIQ SHAIK</u> bearing H.T.No.<u>17709A0401</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>ALTHAF SHAIK</u> bearing H.T.No.<u>17709A0402</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>ANIL VIJAYANAGARAM</u> bearing H.T.No.<u>17709A0403</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>ASHRIT KUMAR SAHU</u> bearing H.T.No.<u>17709A0404</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>CHINNI KRISHNA MADDINA</u> bearing H.T.No.<u>17709A0405</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>GEETHA POLI</u> bearing H.T.No.<u>17709A0406</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020</u> to <u>12.02.2020</u>.



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This is to certify that Mr/Ms. <u>HARITHA PAPPIREDDY</u> bearing H.T.No.<u>17709A0407</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>HARSHA VARDHAN REDDY DYVAM</u> bearing H.T.No.<u>17709A0408</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>HARSHITHA LALAPETA</u> bearing H.T.No.<u>17709A0409</u> has attended a Value Added Course on "<u>Advanced VLSI Concepts</u>" organized by department of ECE during <u>10.02.2020 to 12.02.2020</u>.



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This is to certify that Mr/Ms. <u>AKHILA GUTTA</u> bearing H.T.No. <u>16701A0404</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

VAC Co-ordinator



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This is to certify that Mr/Ms. <u>AMARNATH REDDY BODELA</u> bearing H.T.No. <u>16701A0406</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>AMEEN SUHAIL MS</u> bearing H.T.No. <u>16701A0407</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>ANANTH KUMAR KARNAM BALAIAHGARI</u> bearing H.T.No. <u>16701A0408</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL</u> <u>PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>ANIL CHINTALA</u> bearing H.T.No. <u>16701A0409</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>ANIL KUMAR MADINENI</u> bearing H.T.No. <u>16701A0410</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>ARAVINDANATH REDDY KALAGUTLA</u> bearing H.T.No. <u>16701A0411</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL</u> <u>PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>ARSHIA SHAIK</u> bearing H.T.No. <u>16701A0412</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>ASHOK KUMAR KAVALI</u> bearing H.T.No. <u>16701A0413</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>BALA MANIKANTA BANDARU</u> bearing H.T.No. <u>16701A0414</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>BHARATH KUMAR MEKALA</u> bearing H.T.No. <u>16701A0415</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. **BHARATH REDDY SANEPALLE** bearing H.T.No. **<u>16701A0416</u>** has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. **BHARATHKUMAR REDDY MOLAKATHALLA** bearing H.T.No. <u>16701A0417</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL</u> <u>PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>BHARGHAVA GANDHAM</u> bearing H.T.No. <u>16701A0418</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>CHAITANYA LAHARI BHUMIREDDY</u> bearing H.T.No. <u>16701A0419</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL</u> <u>PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>CHAITHANYA RANGA SAI POLA</u> bearing H.T.No. <u>16701A0420</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>CHANDANA GAJULAPALLE</u> bearing H.T.No. <u>16701A0421</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>CHARITHA REDDY GABBIREDDY</u> bearing H.T.No. <u>16701A0422</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>CHELISHA BUCHAKKAGARI</u> bearing H.T.No. <u>16701A0423</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>CHENNA KRISHNA REDDY GAJJALA</u> bearing H.T.No. <u>16701A0424</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL</u> <u>PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>CHINNA GONGATI</u> bearing H.T.No. <u>16701A0425</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>CHITTEMMA JOHARAPURAM</u> bearing H.T.No. <u>16701A0426</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>DAMODHAR REDDY SINGAM REDDY</u> bearing H.T.No. <u>16701A0427</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL</u> <u>PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>DEEPTHIKIRAN MADAKA</u> bearing H.T.No. <u>16701A0428</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>DHARANI REDDY PONNATHOTA</u> bearing H.T.No. <u>16701A0429</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>DIVYA RAJESWARI PAMULA</u> bearing H.T.No. <u>16701A0430</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>DIVYA VAMULA</u> bearing H.T.No. <u>16701A0431</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>DIVYASREE CHILAMAKURI</u> bearing H.T.No. <u>16701A0432</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>DIWAKAR SUNDARI</u> bearing H.T.No. <u>16701A0433</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. **DURGA PRASAD REDDY BANDI** bearing H.T.No. <u>16701A0434</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>DUSHYANTH REDDY GAADI</u> bearing H.T.No. <u>16701A0435</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>GAYATHRI AVULA YERRAMAREDDY</u> bearing H.T.No. <u>16701A0436</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL</u> <u>PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>GURU SIRISHA MULLINTI</u> bearing H.T.No. <u>16701A0437</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>HARI SIVA PRASAD GANIGIPENTA</u> bearing H.T.No. <u>16701A0438</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL</u> <u>PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>HARISHA BANDARU</u> bearing H.T.No. <u>16701A0439</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>MOHAMMED ISLAM SHAIK</u> bearing H.T.No. <u>16701A0461</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>MOHAN AKKINAGUNTLA</u> bearing H.T.No. <u>16701A0462</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>MOUNIKA DONTHYREDDY</u> bearing H.T.No. <u>16701A0463</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>MOUNIKA GIDDALURU</u> bearing H.T.No. <u>16701A0464</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>NARAHARI GIDDALURU</u> bearing H.T.No. <u>16701A0465</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>NARASIMHA REDDY KATHA</u> bearing H.T.No. <u>16701A0466</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>NARAYANA ALURI</u> bearing H.T.No. <u>16701A0467</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>NAVEEN KUMAR KAMINI</u> bearing H.T.No. <u>16701A0468</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>NAVEEN KUMAR REDDY SANGANA</u> bearing H.T.No. <u>16701A0469</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL</u> <u>PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>NEERAJAKSHI CHINTHA</u> bearing H.T.No. <u>16701A0470</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>NIHARIKA PEMMANA</u> bearing H.T.No. <u>16701A0471</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>PAVAN REDDY PALAKOLANU</u> bearing H.T.No. <u>16701A0472</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>PAVANKUMAR NAIDU CHITRAJU</u> bearing H.T.No. <u>16701A0473</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>PAVANKUMAR RAJU RAGHAVARAJU</u> bearing H.T.No. <u>16701A0474</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL</u> <u>PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>PAVITHRA PALA</u> bearing H.T.No. <u>16701A0475</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>PENCHALA PRAVEEN BADABHAGNI</u> bearing H.T.No. <u>16701A0476</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL</u> <u>PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SAI SADHANA HANUMANTHAKARI</u> bearing H.T.No. <u>16701A04A3</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL</u> <u>PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SAI SIVARAM NAGENDLA</u> bearing H.T.No. <u>16701A04A4</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SAI VIGNESH R</u> bearing H.T.No. <u>16701A04A5</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SAITEJA DUGGINENI</u> bearing H.T.No. <u>16701A04A6</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SANDHYA RANI SYAMANABOYANA</u> bearing H.T.No. <u>16701A04A7</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL</u> <u>PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SANJEEVULU PODAGANDLA</u> bearing H.T.No. <u>16701A04A8</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SANTHOSH NAIK RAMAVATH</u> bearing H.T.No. <u>16701A04A9</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SATEESH KUMAR SOLIGA</u> bearing H.T.No. <u>16701A04B0</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SATHEESH KUMAR SANDULO</u> bearing H.T.No. <u>16701A04B1</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SATISH TALARI</u> bearing H.T.No. <u>16701A04B2</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SHAMEERBASHA DUDEKULA</u> bearing H.T.No. <u>16701A04B4</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SISINDRINATH REDDY THAVISHELA</u> bearing H.T.No. <u>16701A04B5</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL</u> <u>PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SIVA KRISHNA VALLEPU</u> bearing H.T.No. <u>16701A04B6</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SIVA PRASAD REDDY BANDI</u> bearing H.T.No. <u>16701A04B7</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SIVA REDDY NALLACHIGARI</u> bearing H.T.No. <u>16701A04B8</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SIVASANKAR NAIK BUKKE</u> bearing H.T.No. <u>16701A04B9</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SONIA KADAPA</u> bearing H.T.No. <u>16701A04C1</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SREENIVASULU NALLAMARU</u> bearing H.T.No. <u>16701A04C2</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SRIKANTH REDDY DEVIREDDY</u> bearing H.T.No. <u>16701A04C3</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SUCHITHRA UPPU</u> bearing H.T.No. <u>16701A04C4</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SUHAIL SHAIK</u> bearing H.T.No. <u>16701A04C5</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SUJA OBILI</u> bearing H.T.No. <u>16701A04C6</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SUMANTH GUDISE</u> bearing H.T.No. <u>16701A04C7</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SUPRAJA ELECHARALA</u> bearing H.T.No. <u>16701A04C8</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SUPRIYA BHUPALAM</u> bearing H.T.No. <u>16701A04C9</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>SUPRIYA SAAREDDY</u> bearing H.T.No. <u>16701A04D0</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>VENKATESWARLU KAMMANETHI</u> bearing H.T.No. <u>16701A04F0</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL</u> <u>PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>VISHNU SAI GUTTY</u> bearing H.T.No. <u>16701A04F2</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>VISHNU TEJA NALLA</u> bearing H.T.No. <u>16701A04F3</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>VISHNUVARDHINI PASUPULETI</u> bearing H.T.No. <u>16701A04F4</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>VISHWAJA AKEPATI</u> bearing H.T.No. <u>16701A04F5</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>VISHWAJA REDDY B</u> bearing H.T.No. <u>16701A04F6</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>VISWANATH CHALAMALA</u> bearing H.T.No. <u>16701A04F7</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>YOUNUS HUSSAIN SHAIK</u> bearing H.T.No. <u>16701A04F8</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>VARADA REDDY KUNDLA</u> bearing H.T.No. <u>17705A0408</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>HIMABINDU POOLAMALLIREDDY</u> bearing H.T.No. <u>16709A0408</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL</u> <u>PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. **KARTHIK CHOWDARY MASINA** bearing H.T.No. <u>16709A0409</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>KUMAR TEJA ANANDALA</u> bearing H.T.No. <u>16709A0410</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>KUSUMA ALIDENA</u> bearing H.T.No. <u>16709A0411</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>LAKSHMI NARASIMHA SWAMY PABBISETTI</u> bearing H.T.No. <u>16709A0412</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL</u> <u>PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. LAKSHMI VARAPRASAD CHALLA bearing H.T.No. <u>16709A0413</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>MADHUKAR MADALLAPALLE</u> bearing H.T.No. <u>16709A0415</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>MAHESWARI DESU</u> bearing H.T.No. <u>16709A0416</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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This is to certify that Mr/Ms. <u>MALLESWAR REDDY GAJJALA</u> bearing H.T.No. <u>16709A0417</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

VAC Co-ordinator



DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

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VALUE ADDED COURSE

CERTIFICATE OF APPRECIATION

This is to certify that Mr/Ms. <u>MALLIKARJUN BOMMU</u> bearing H.T.No. <u>16709A0418</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

VAC Co-ordinator



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VALUE ADDED COURSE

CERTIFICATE OF APPRECIATION

This is to certify that Mr/Ms. <u>MANIKANTA REDDY DUMPALA</u> bearing H.T.No. <u>16709A0419</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

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VALUE ADDED COURSE

CERTIFICATE OF APPRECIATION

This is to certify that Mr/Ms. <u>NAVEEN KUMAR REDDY BANDI</u> bearing H.T.No. <u>16709A0420</u> has attended a Value Added Course on "<u>ADVANCES IN DIGITAL SIGNAL PROCESSING</u>" organized by department of ECE during <u>03.02.2020 to 05.02.2020</u>.

VAC Co-ordinator